

FIG. 1

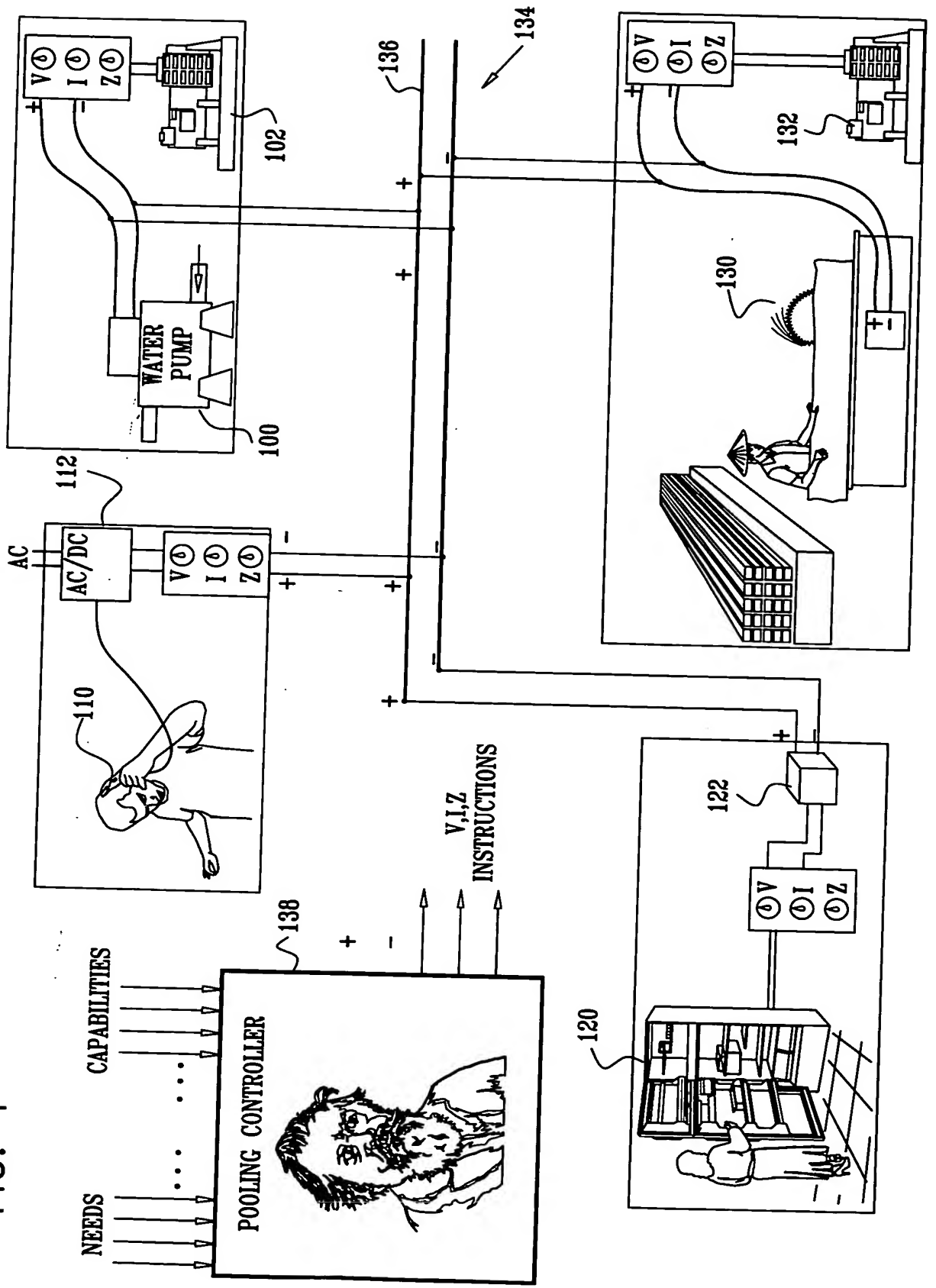


FIG. 2

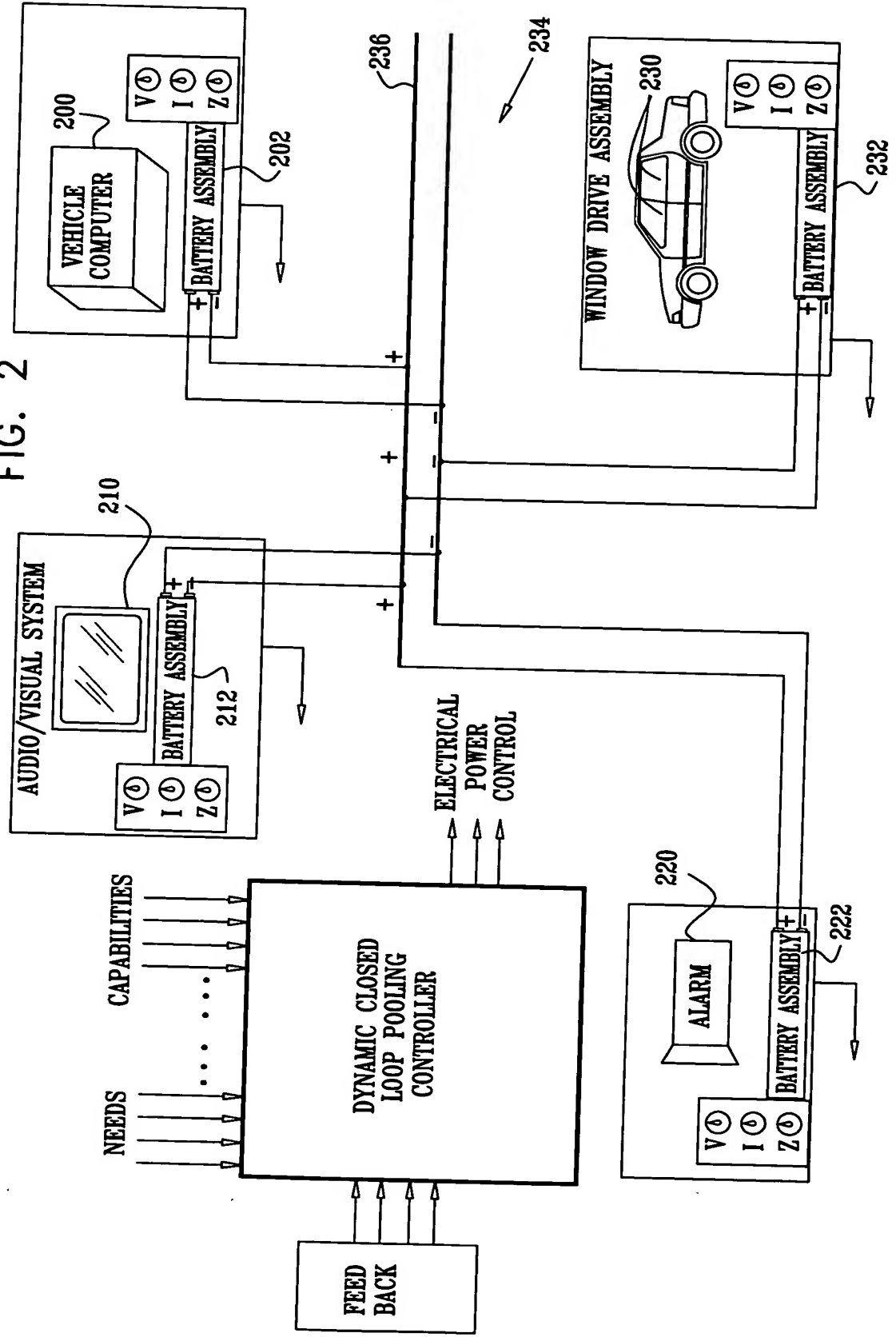


FIG. 3

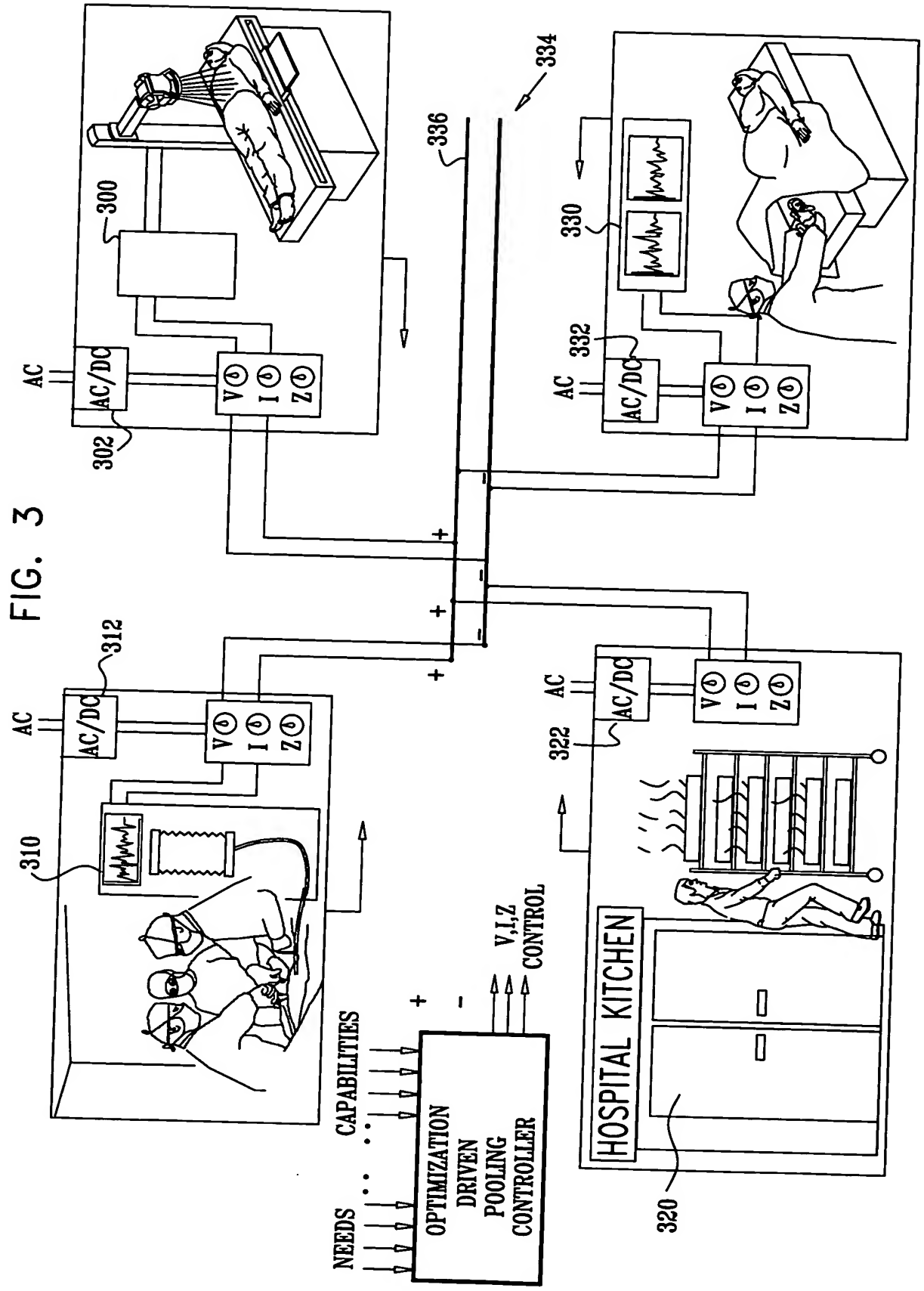
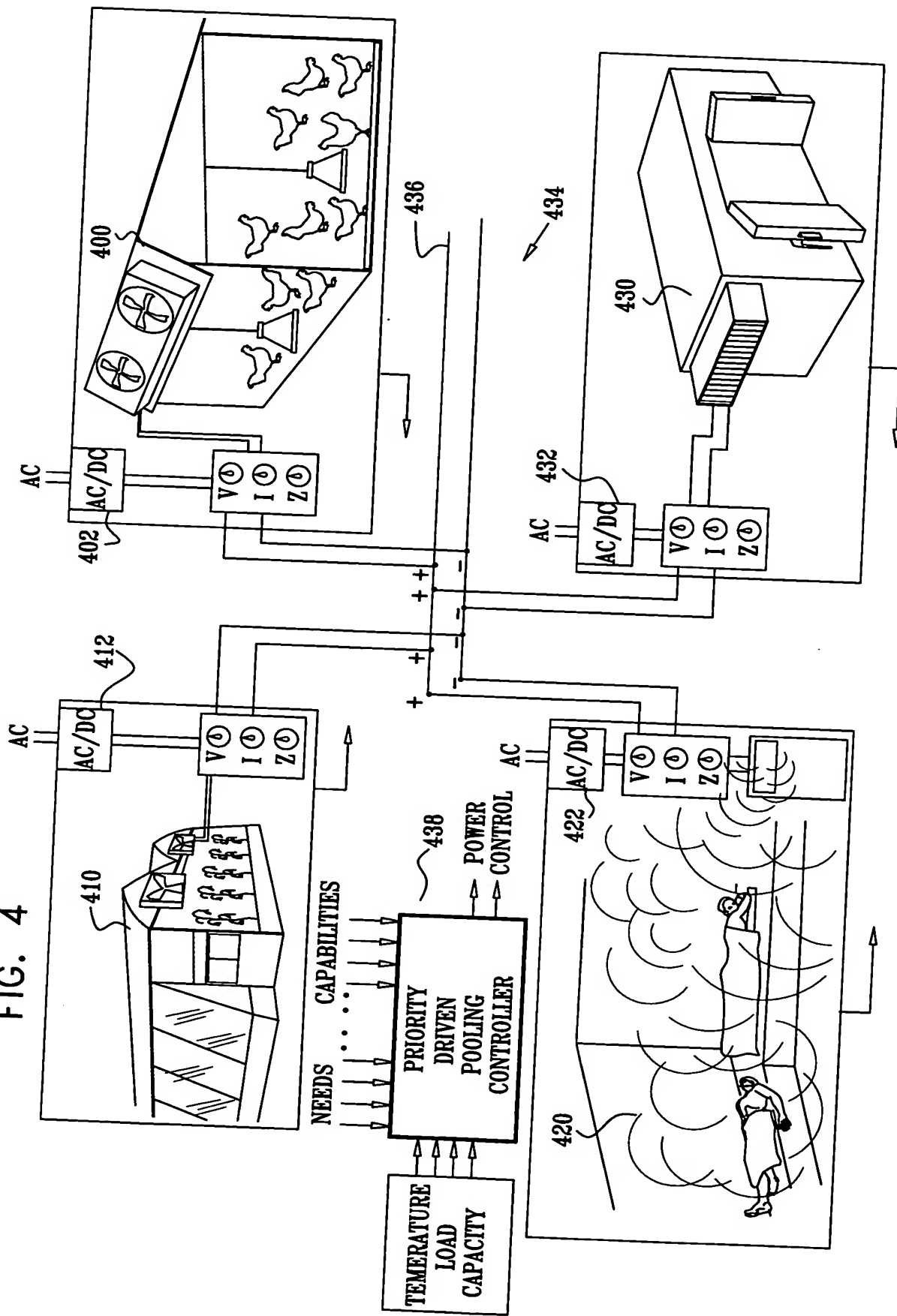


FIG. 4



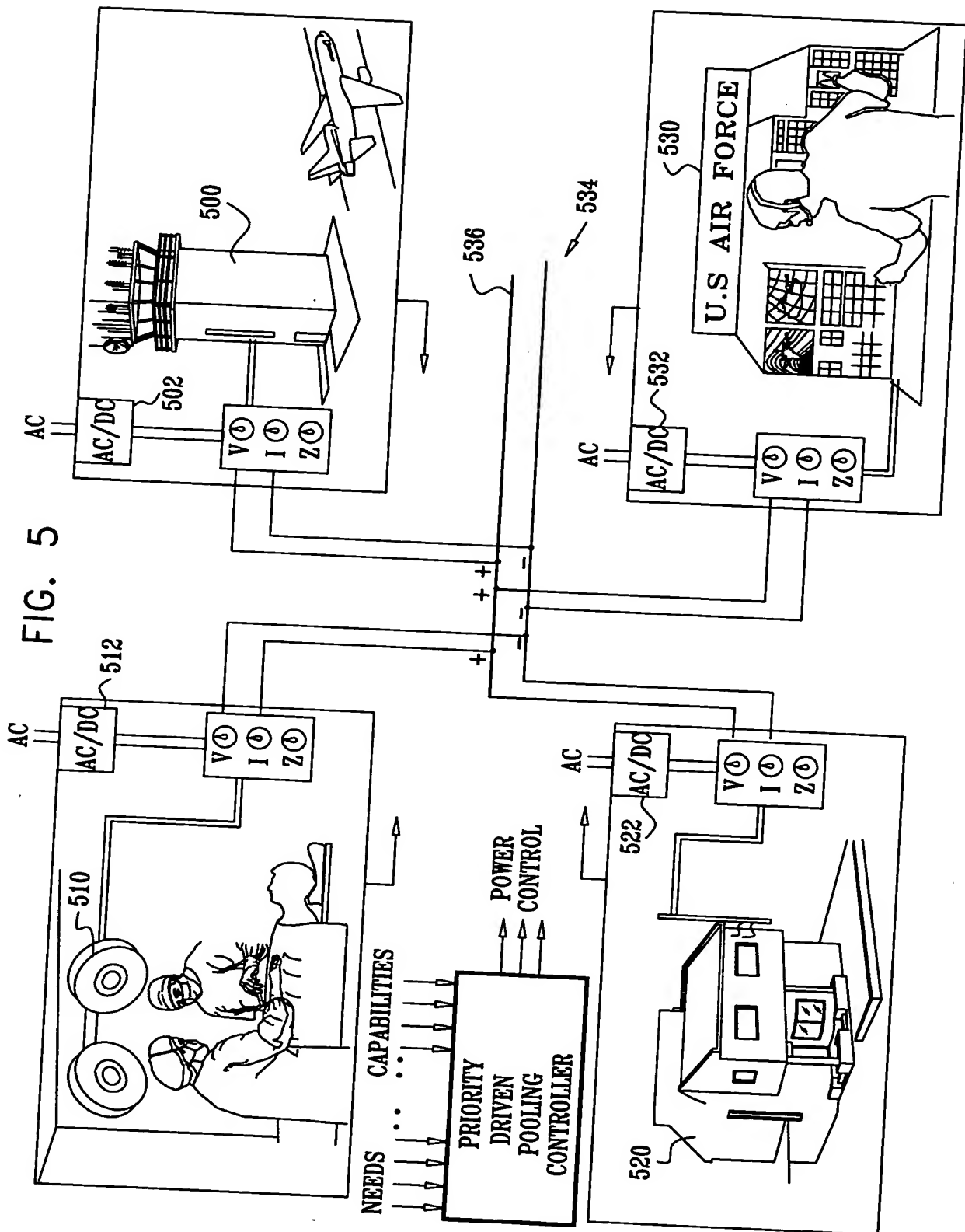


FIG. 5

FIG. 6

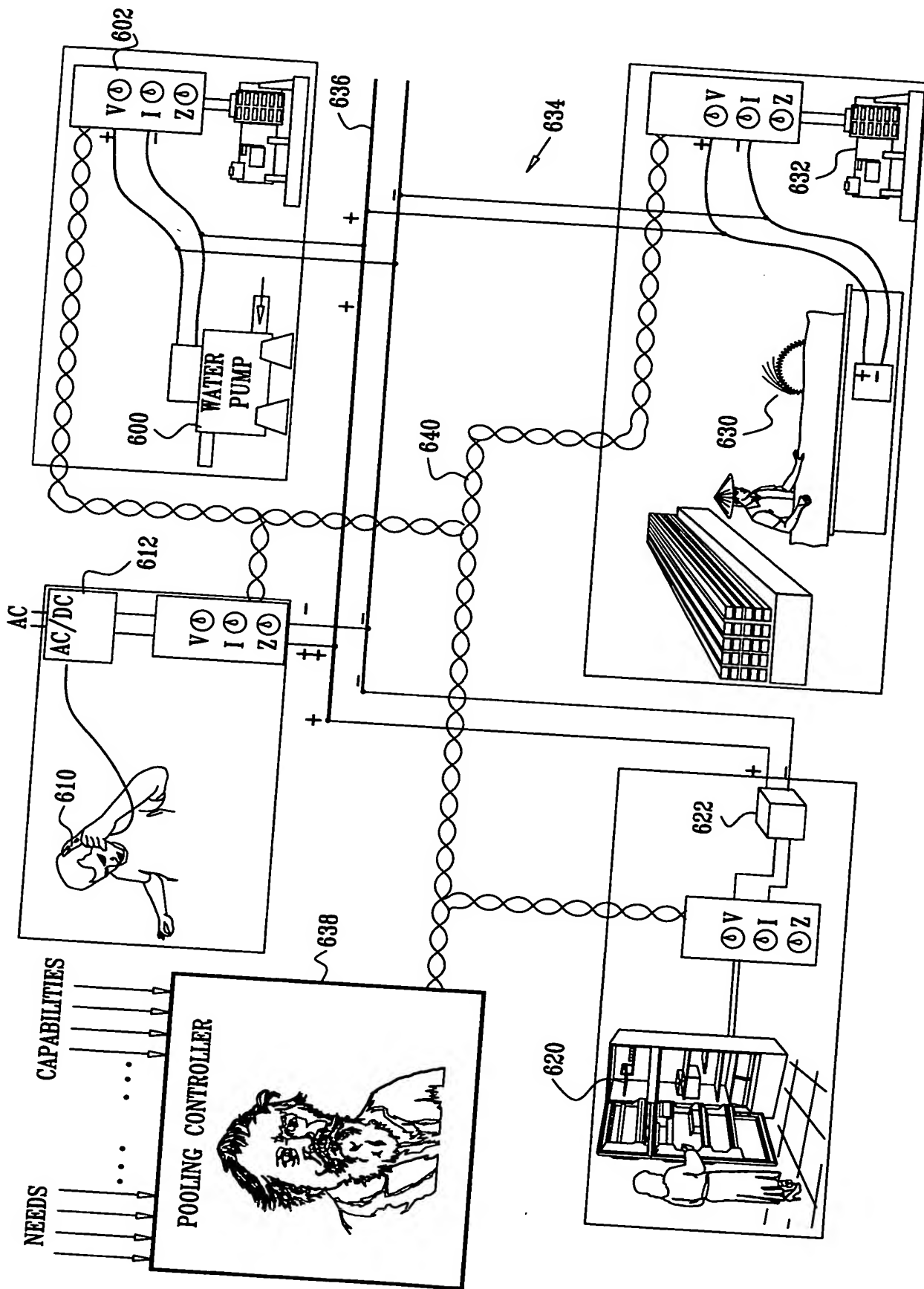


FIG. 7

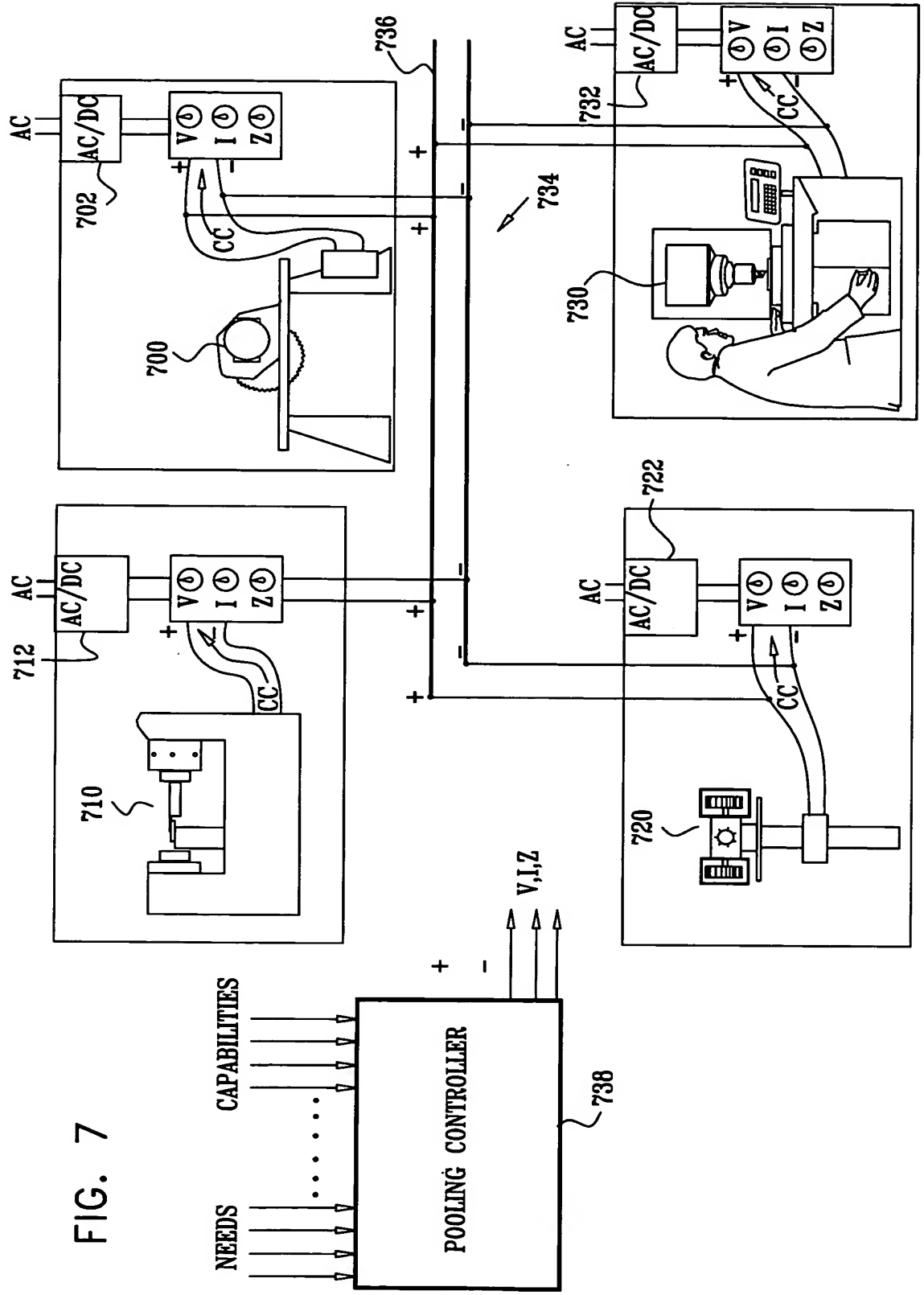


FIG. 8

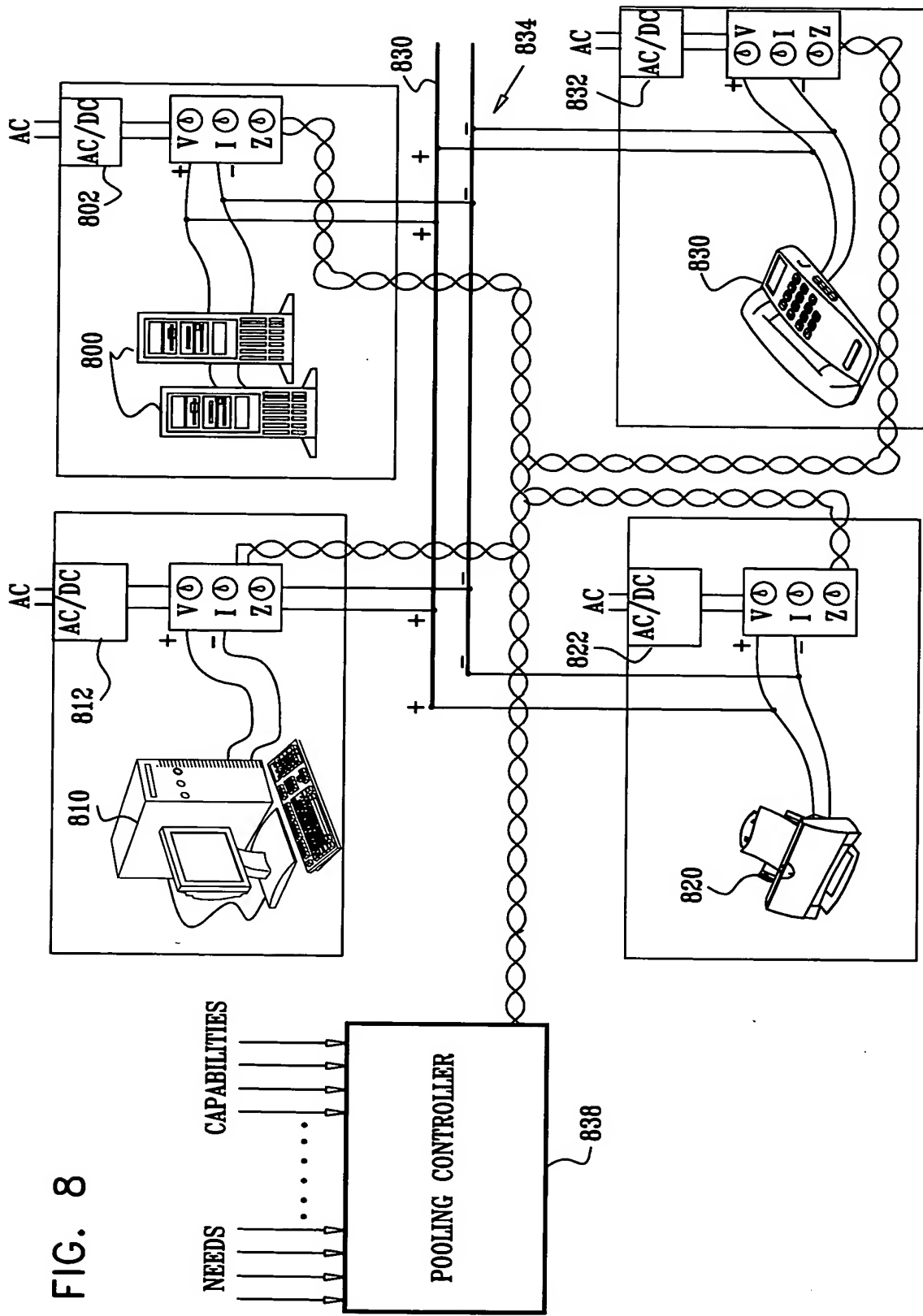


FIG. 9

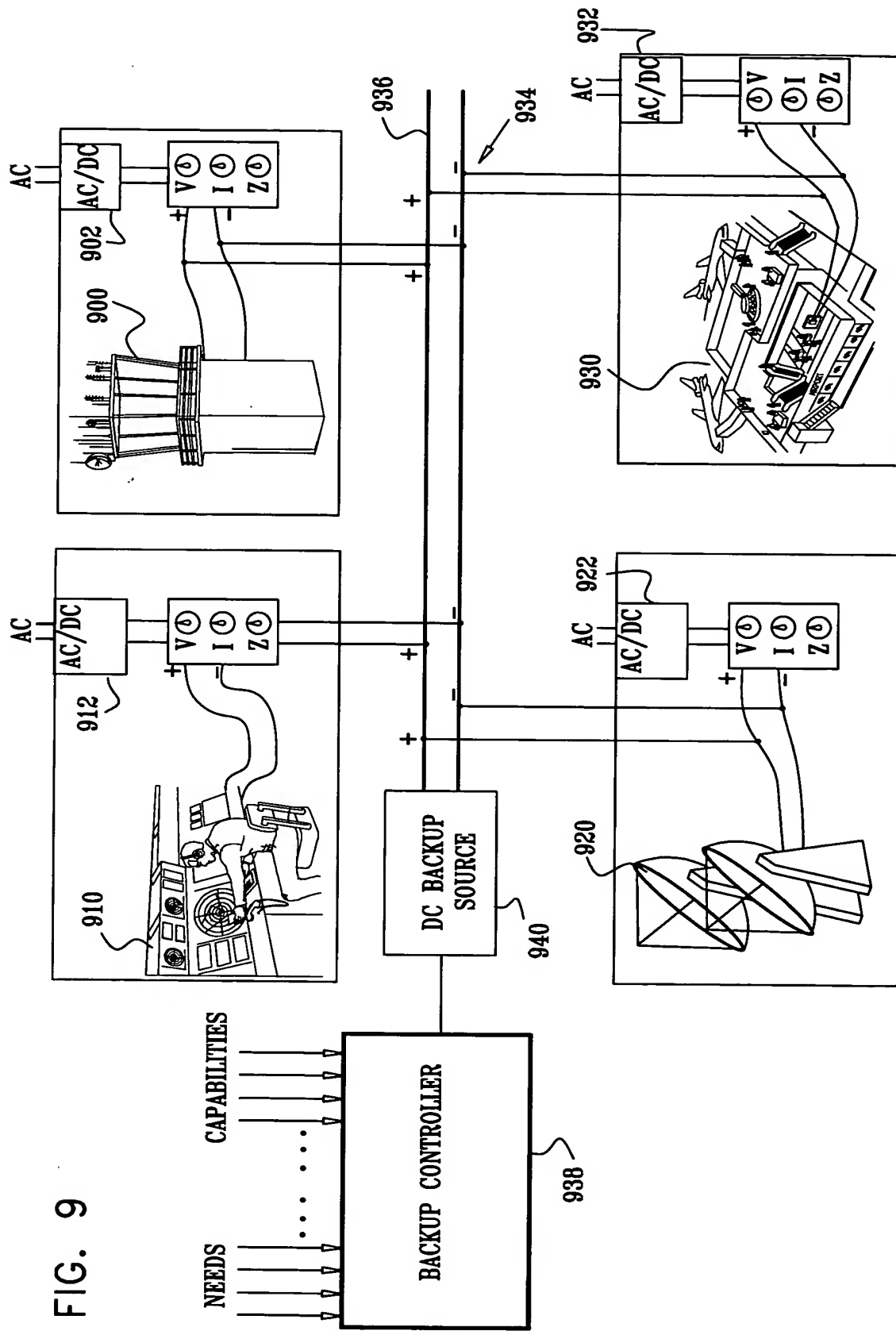
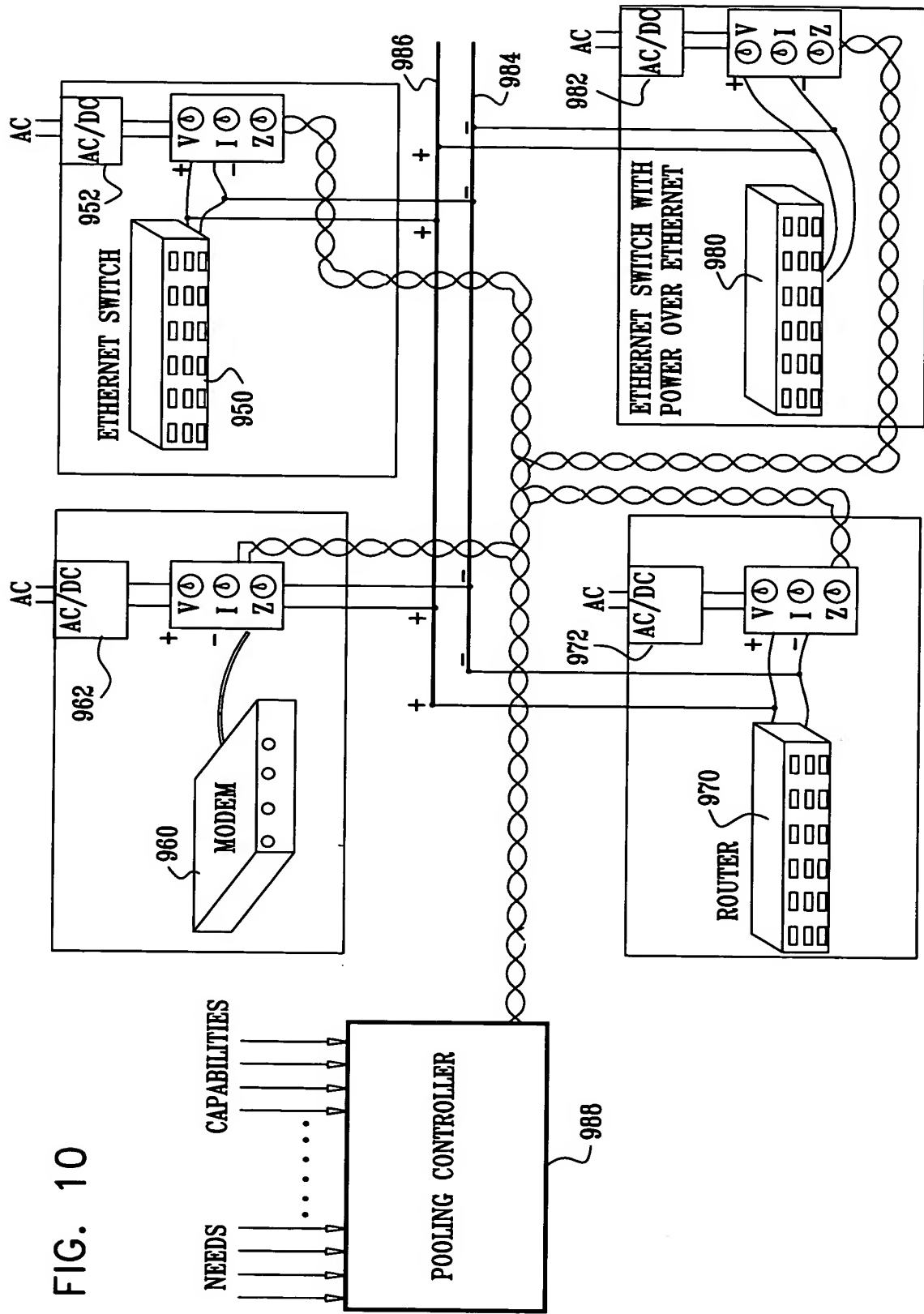


FIG. 10



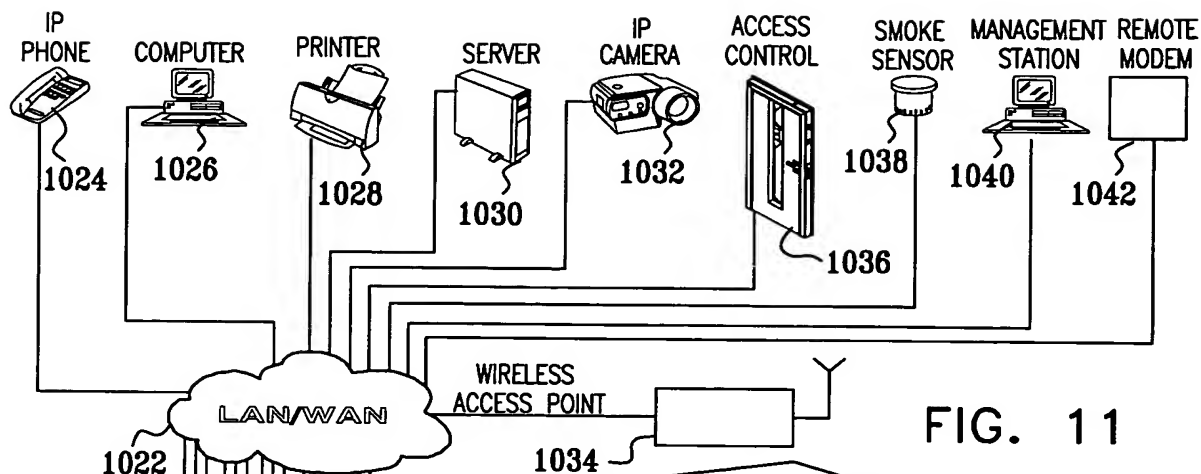
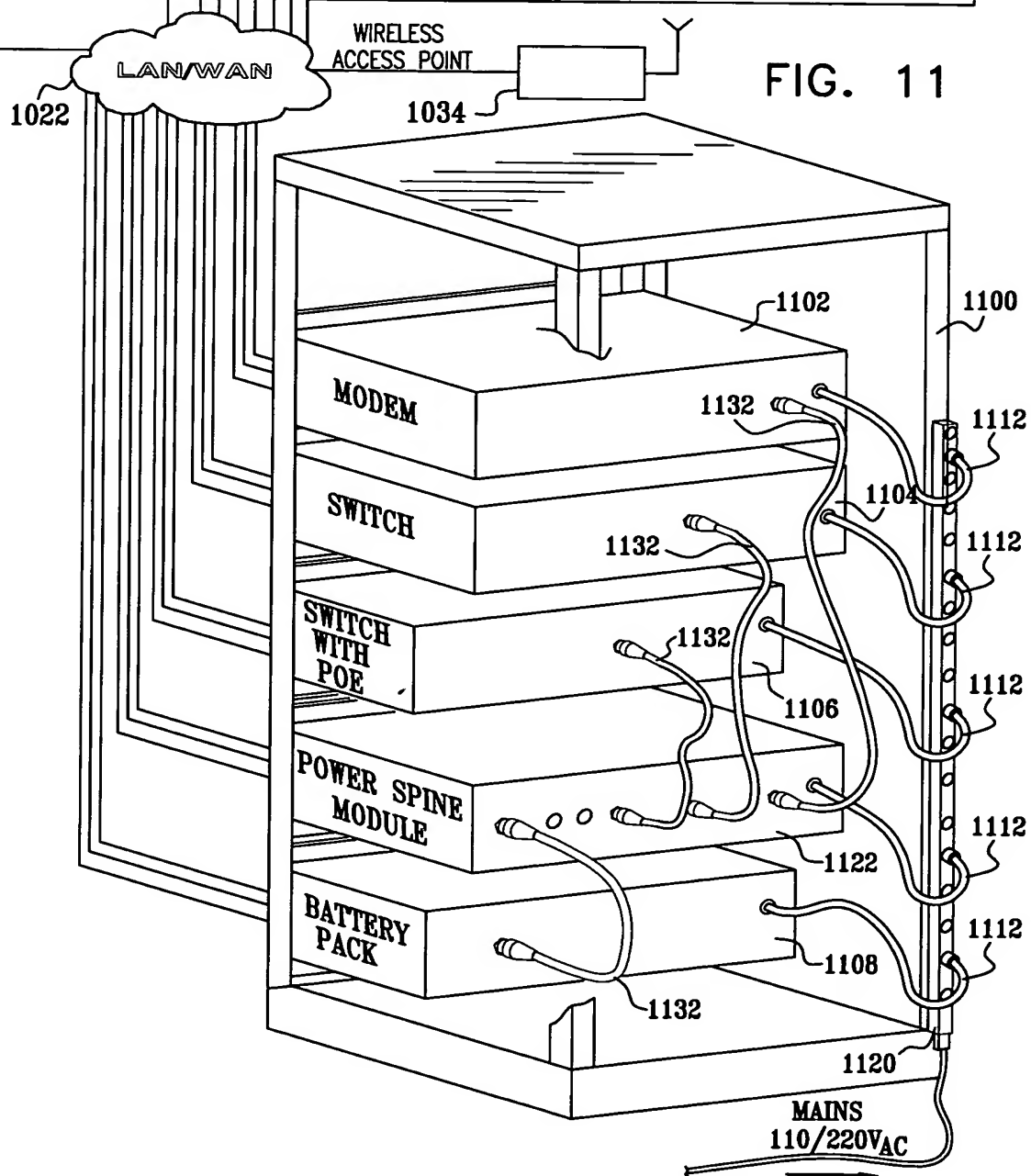
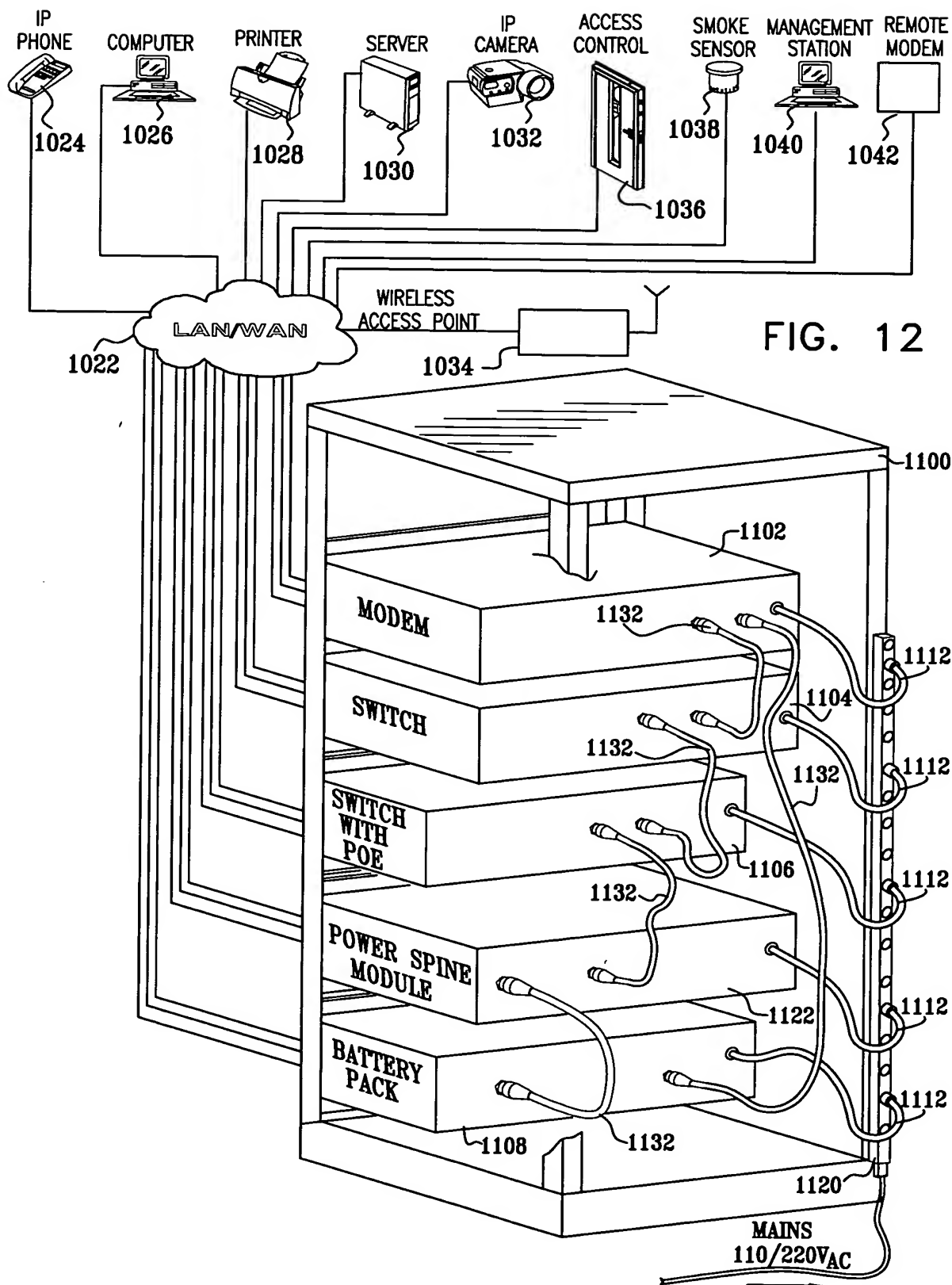


FIG. 11





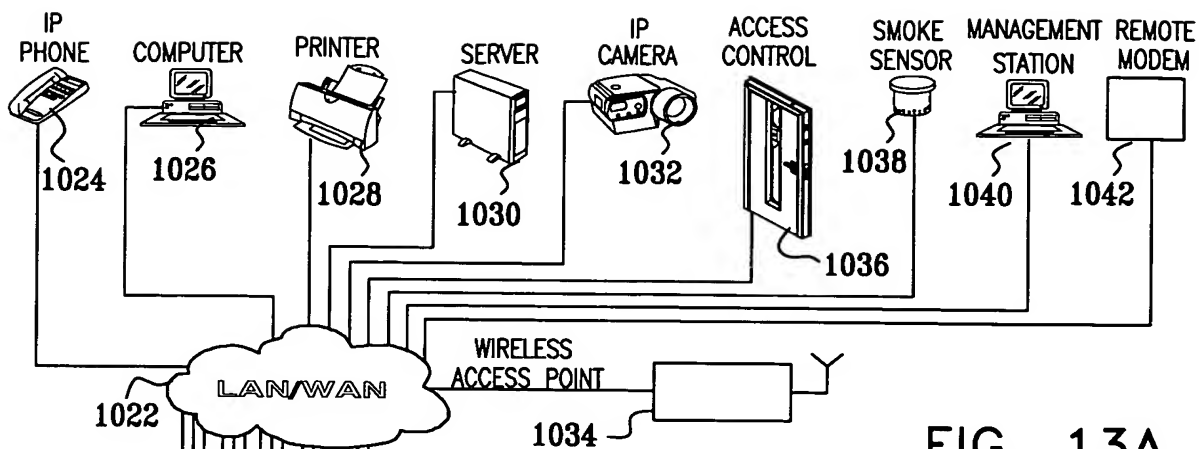
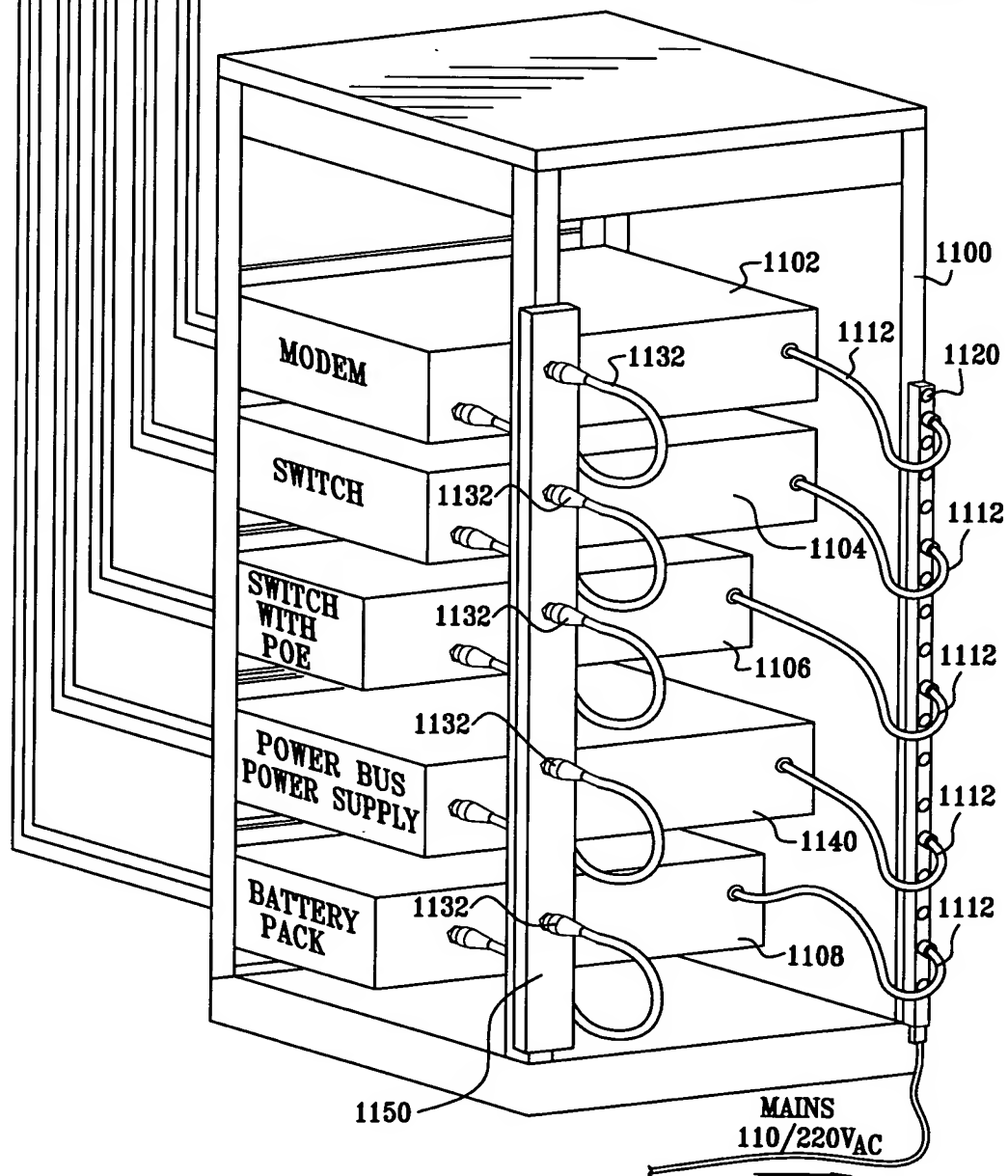


FIG. 13A



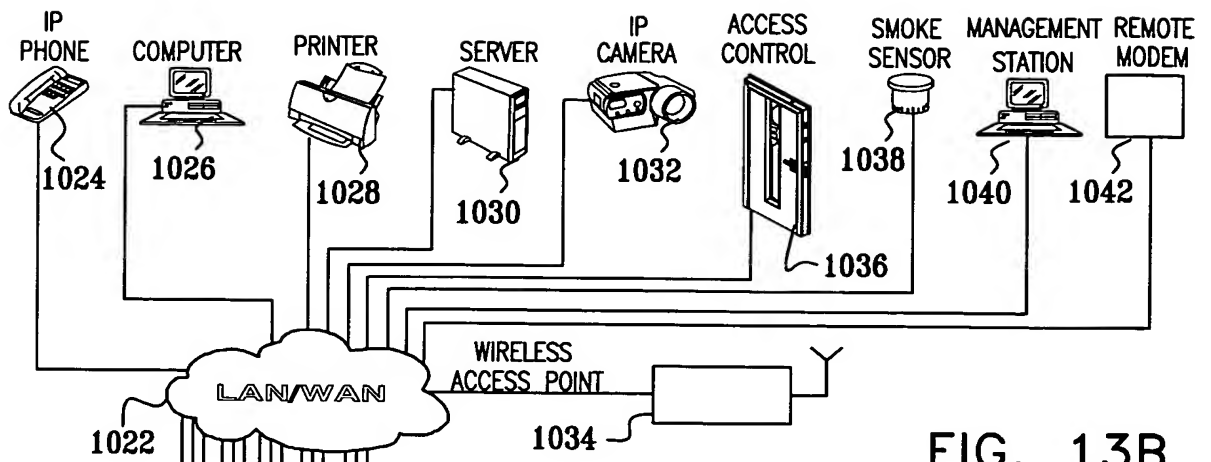
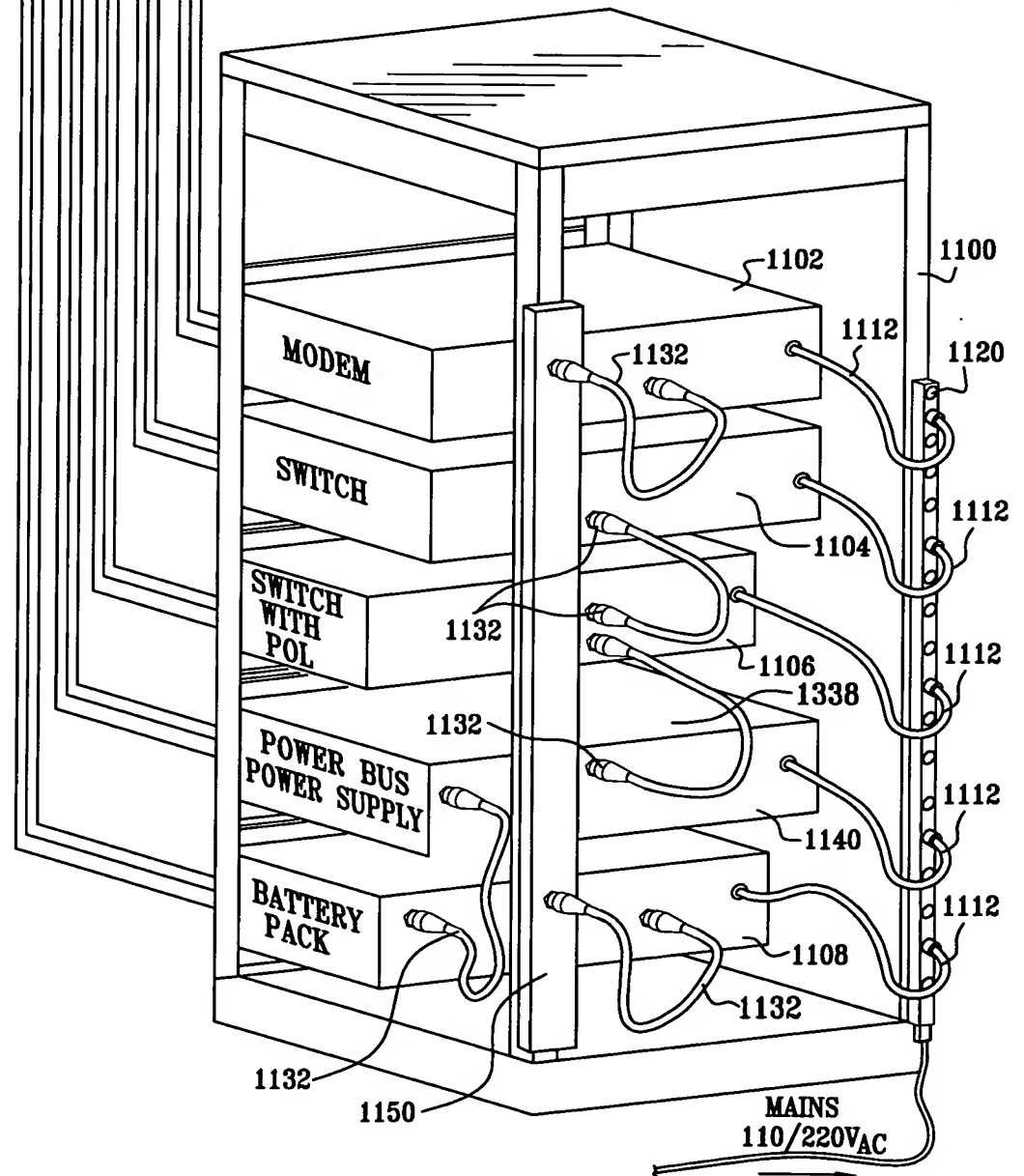
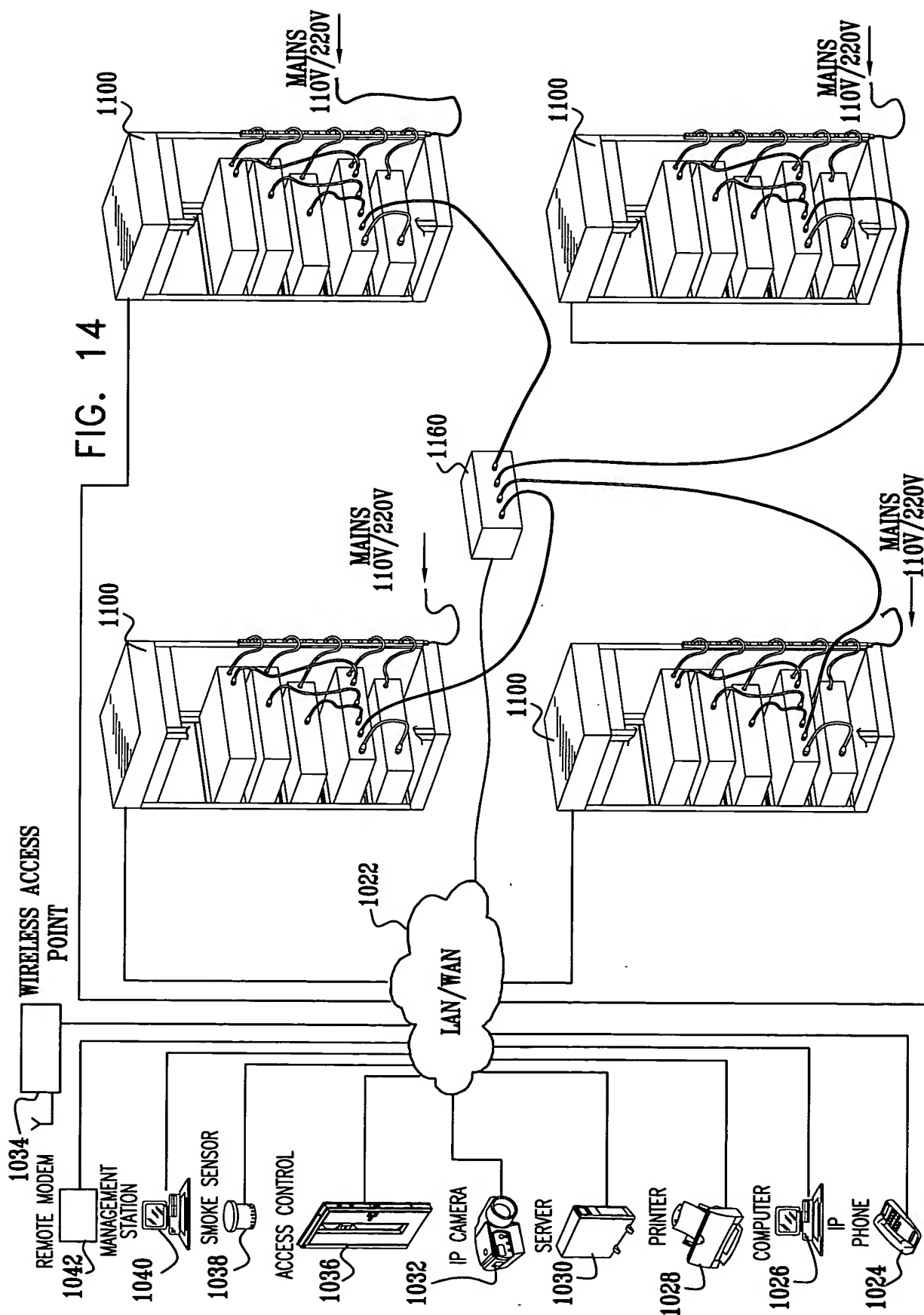
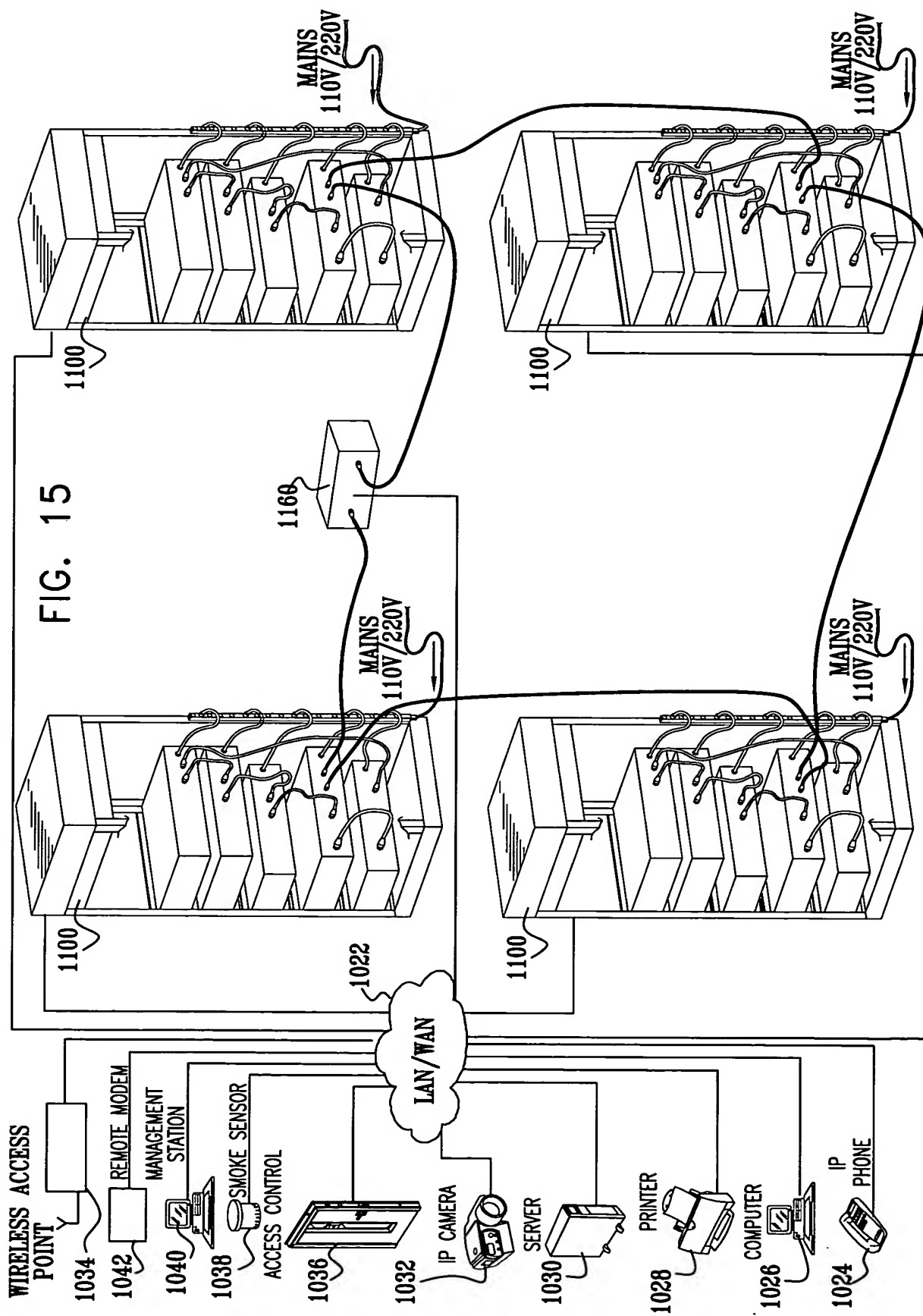


FIG. 13B







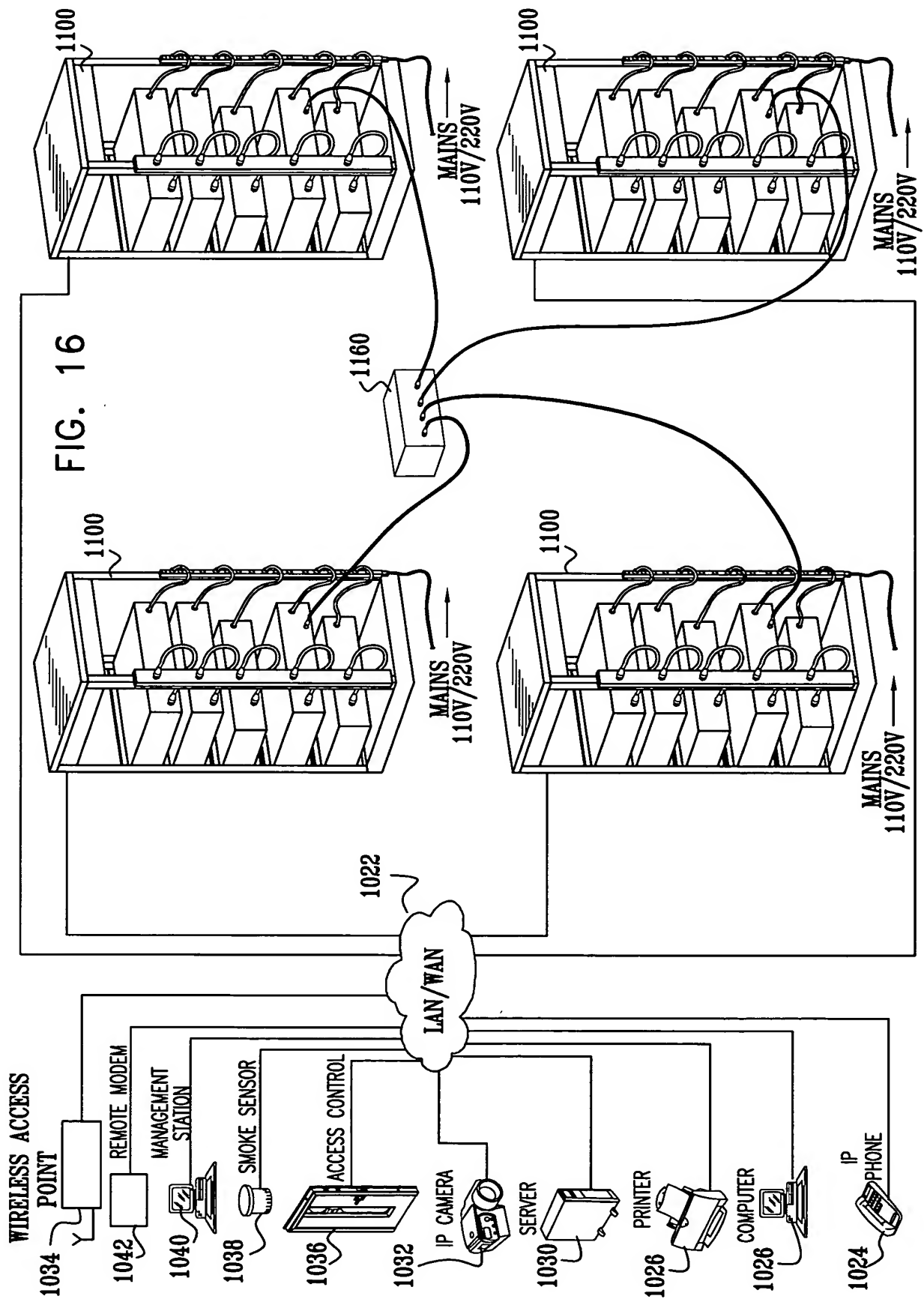


FIG. 17

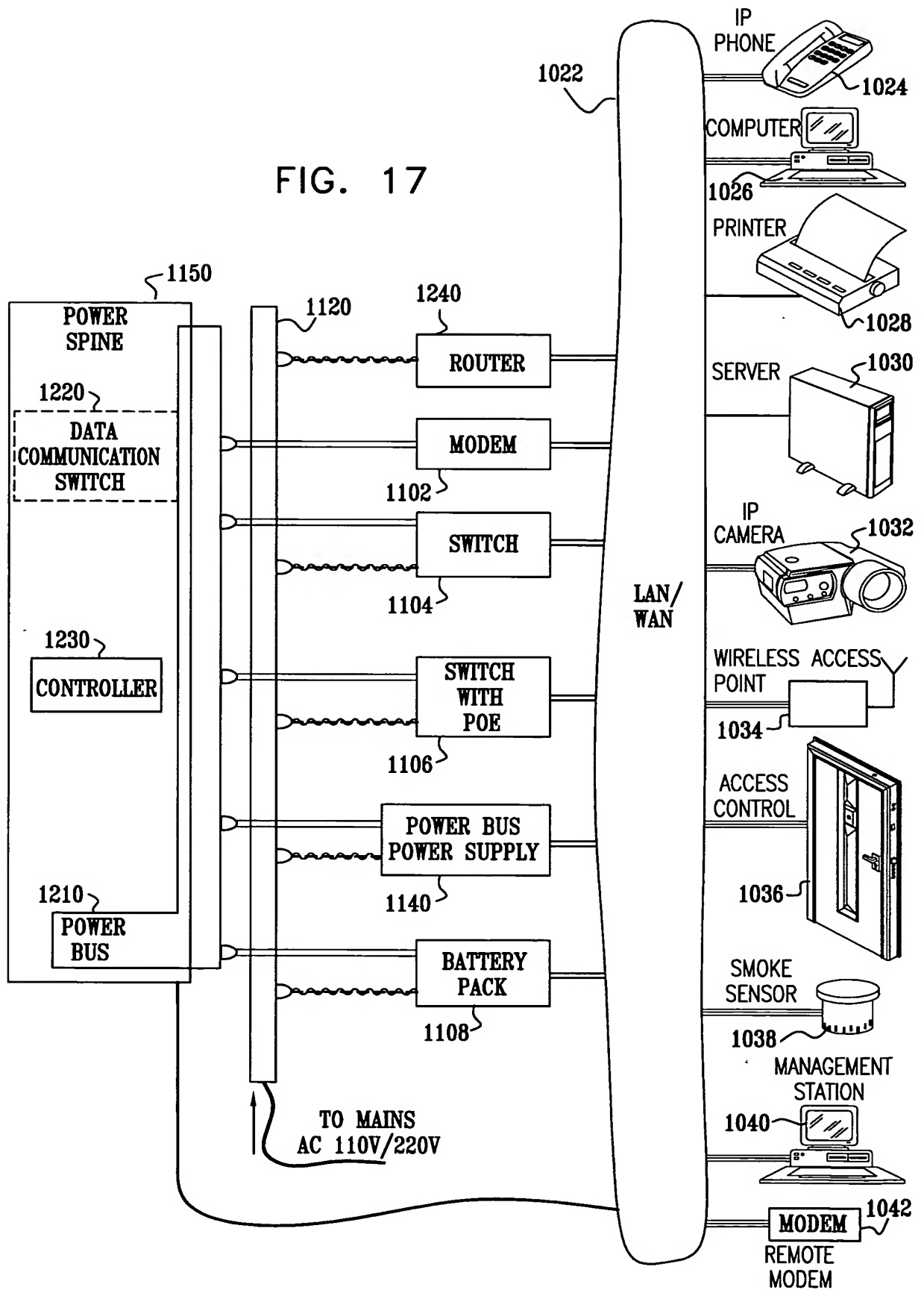


FIG. 18A

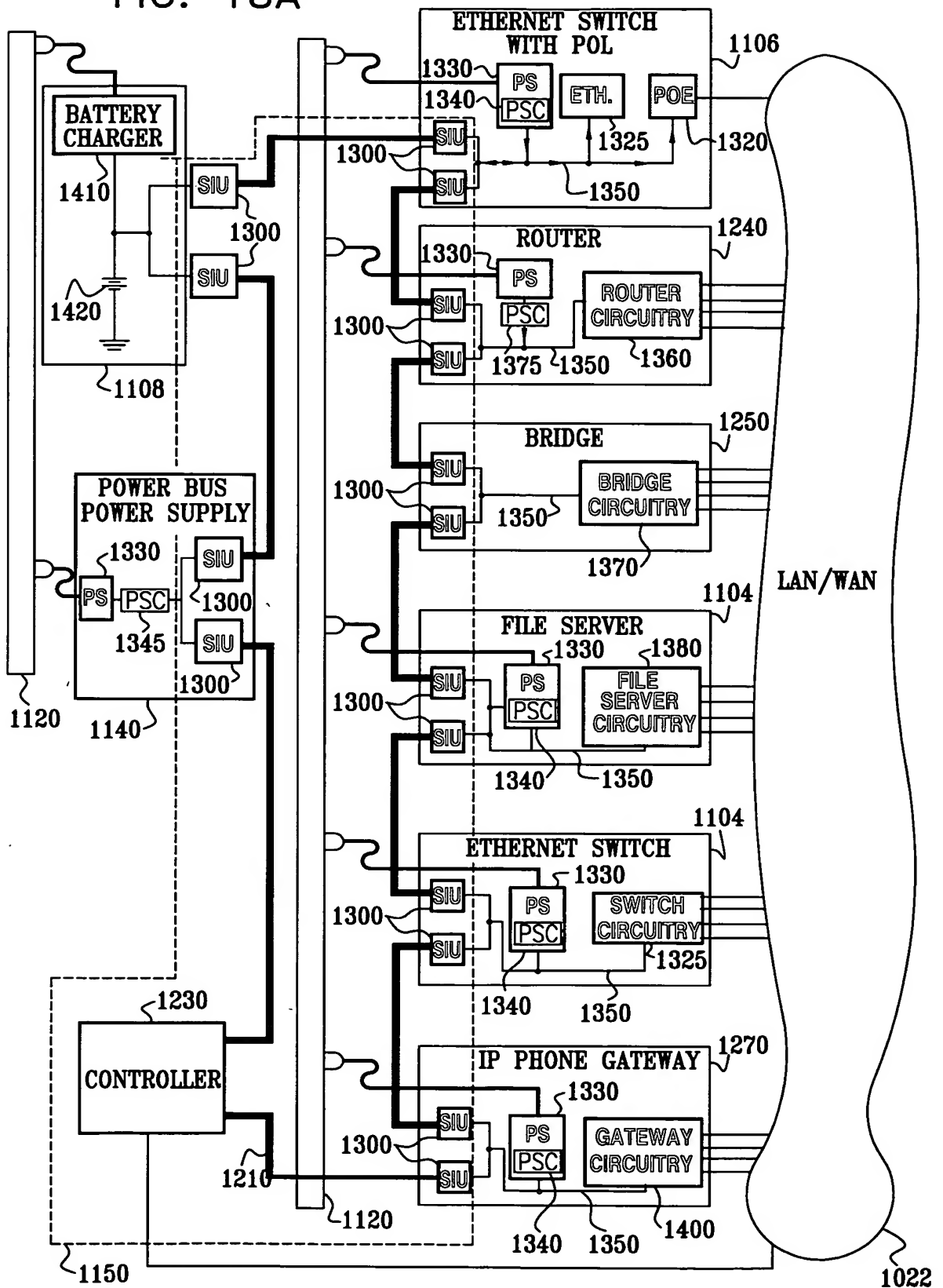


FIG. 18B

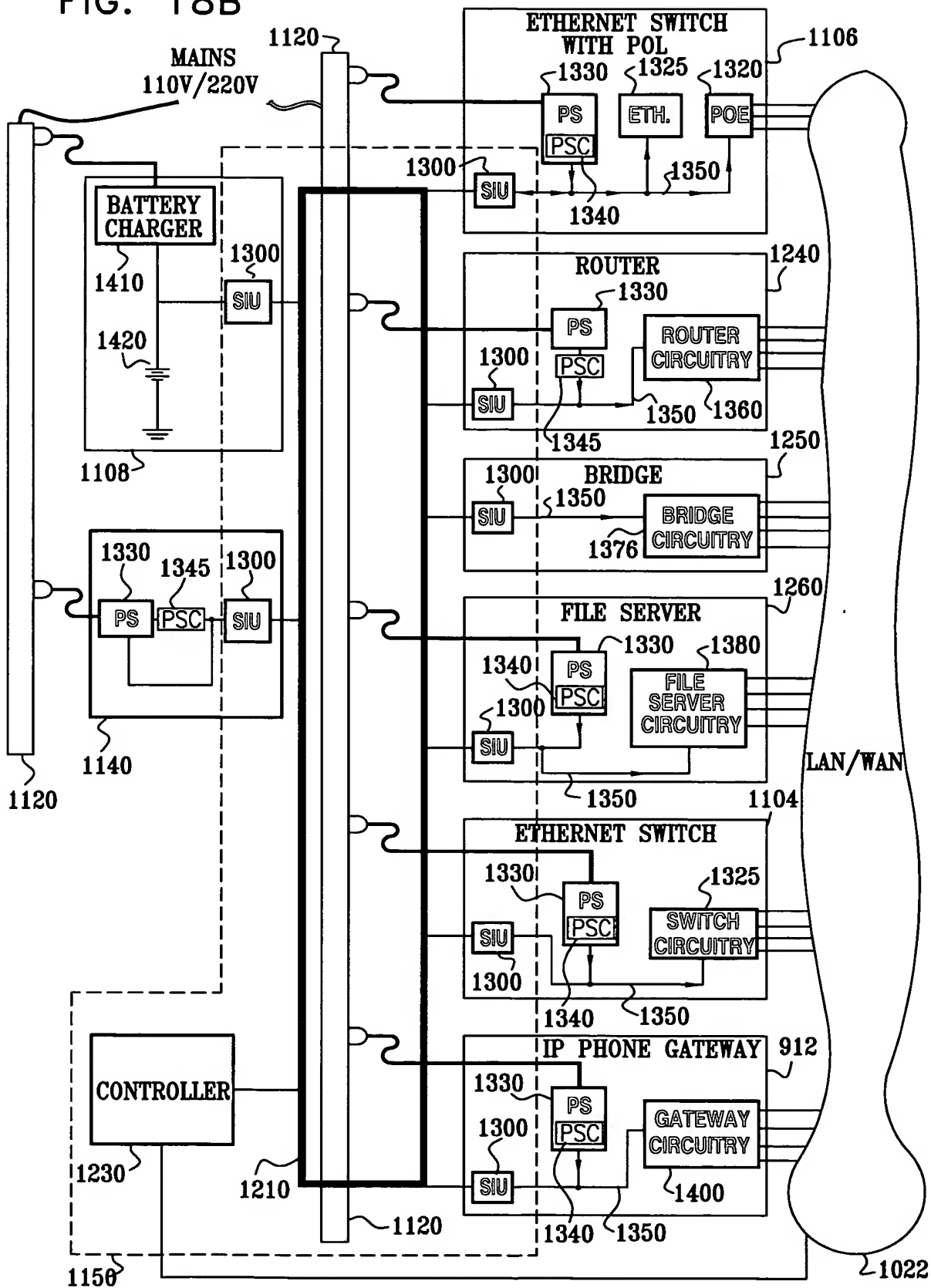


FIG. 19A

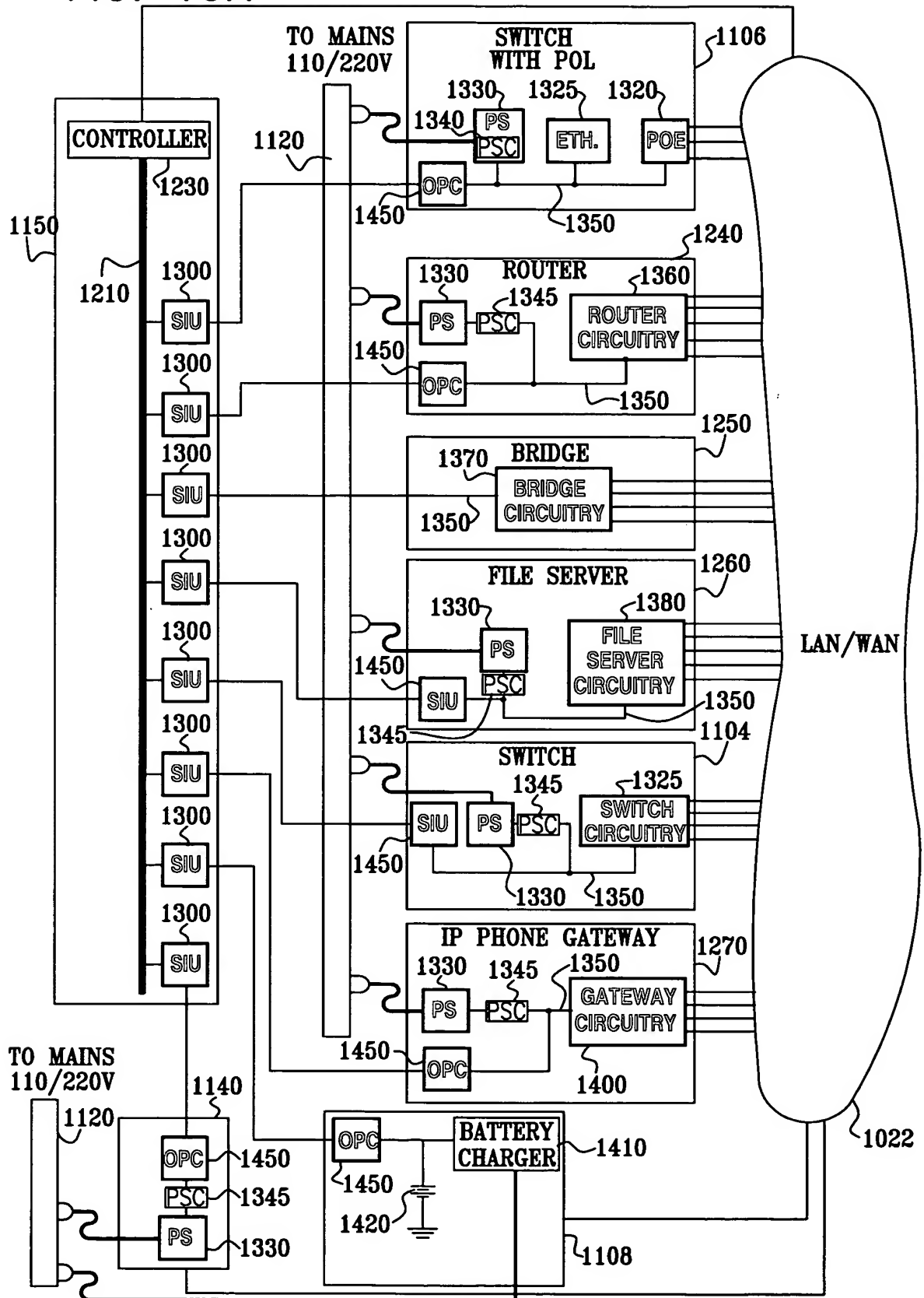


FIG. 19B

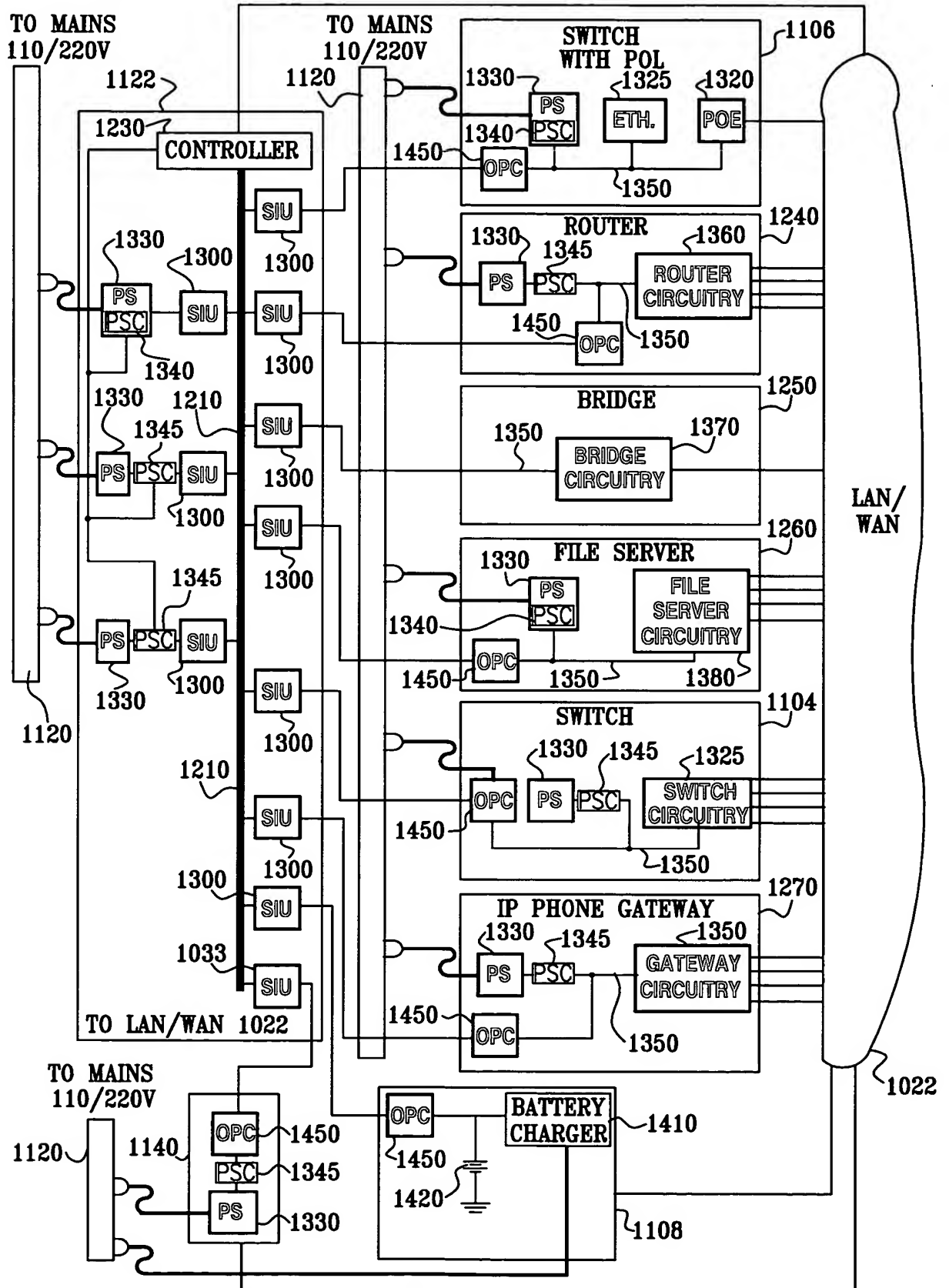


FIG. 20A

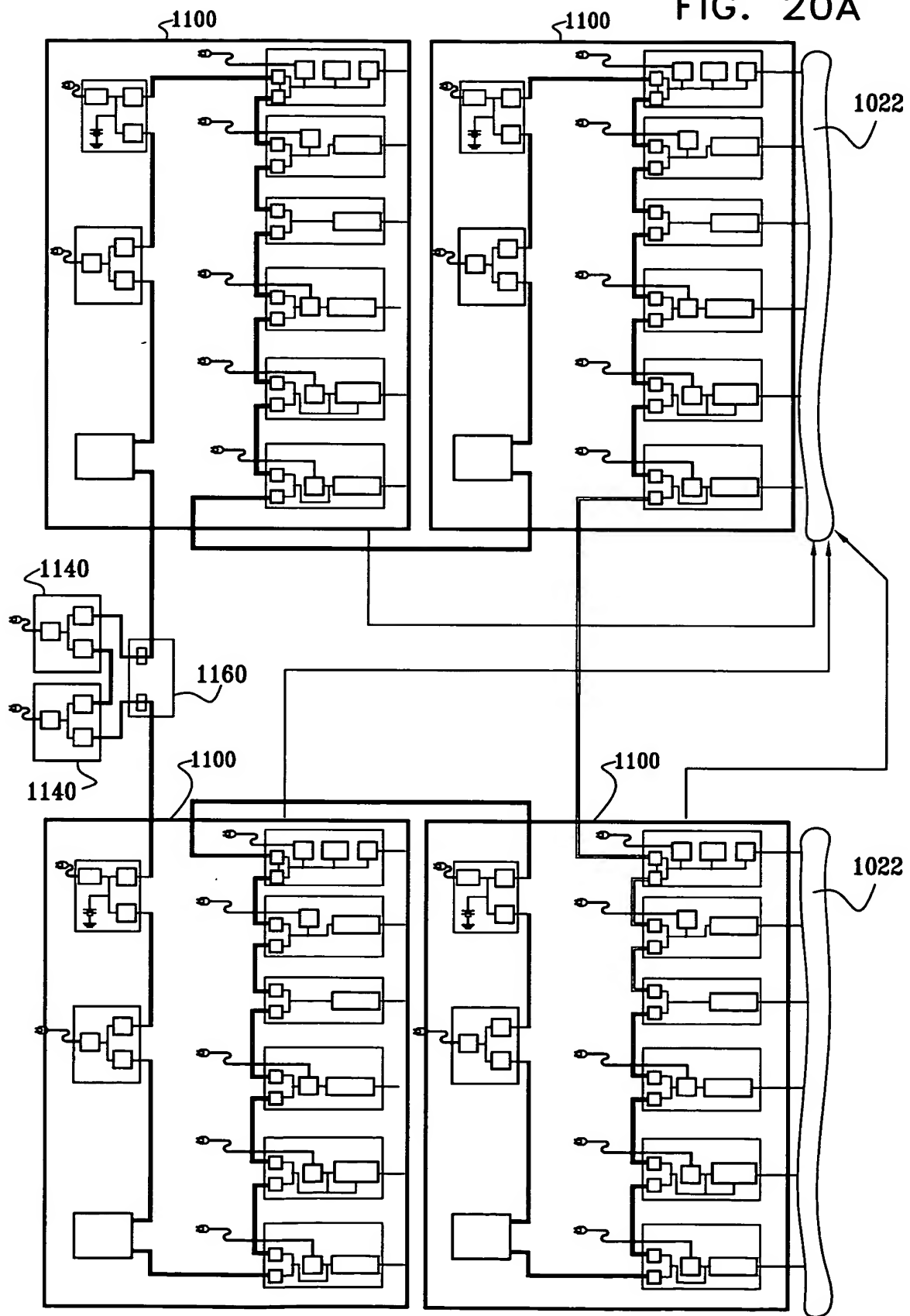


FIG. 20B

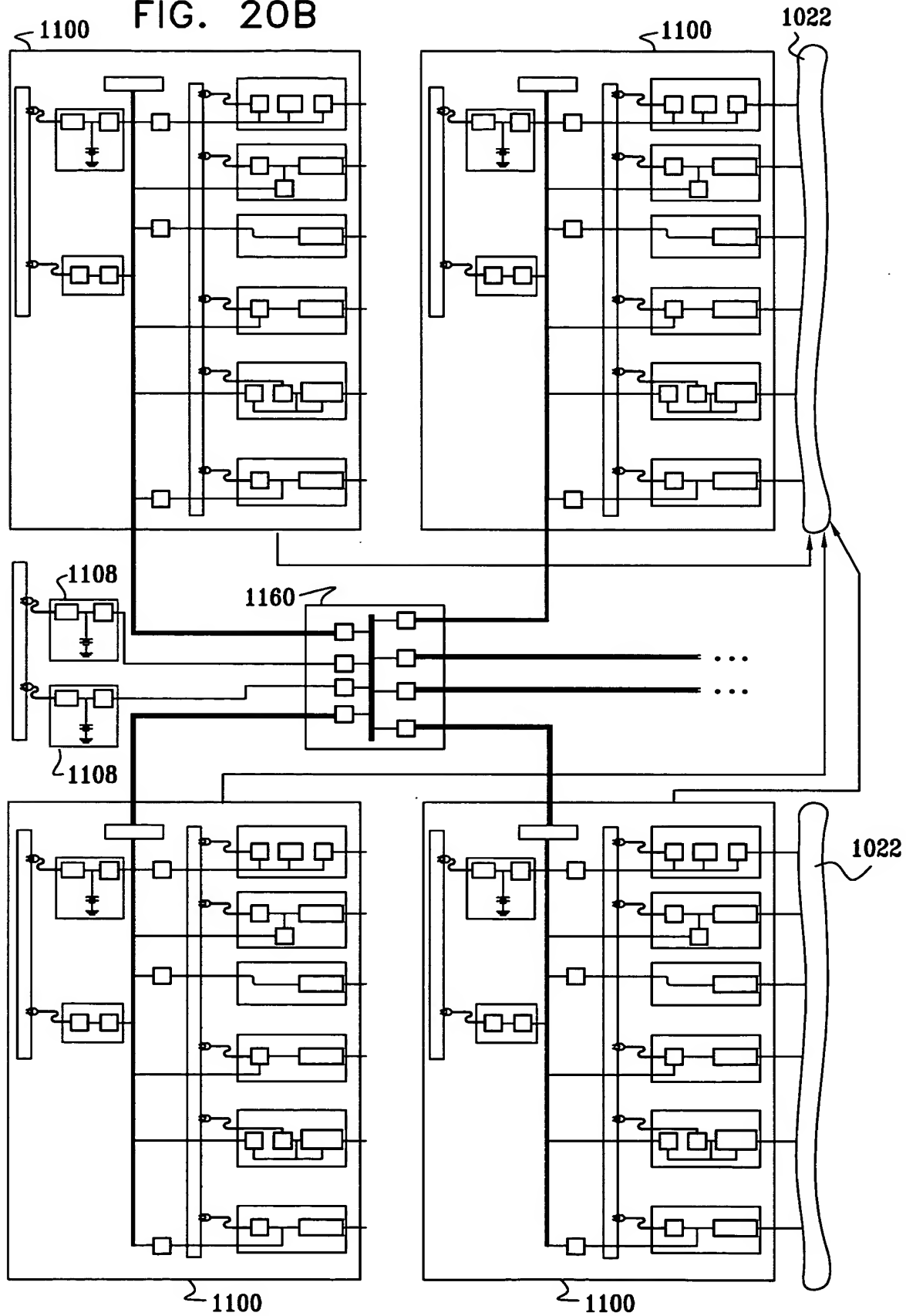


FIG. 21A

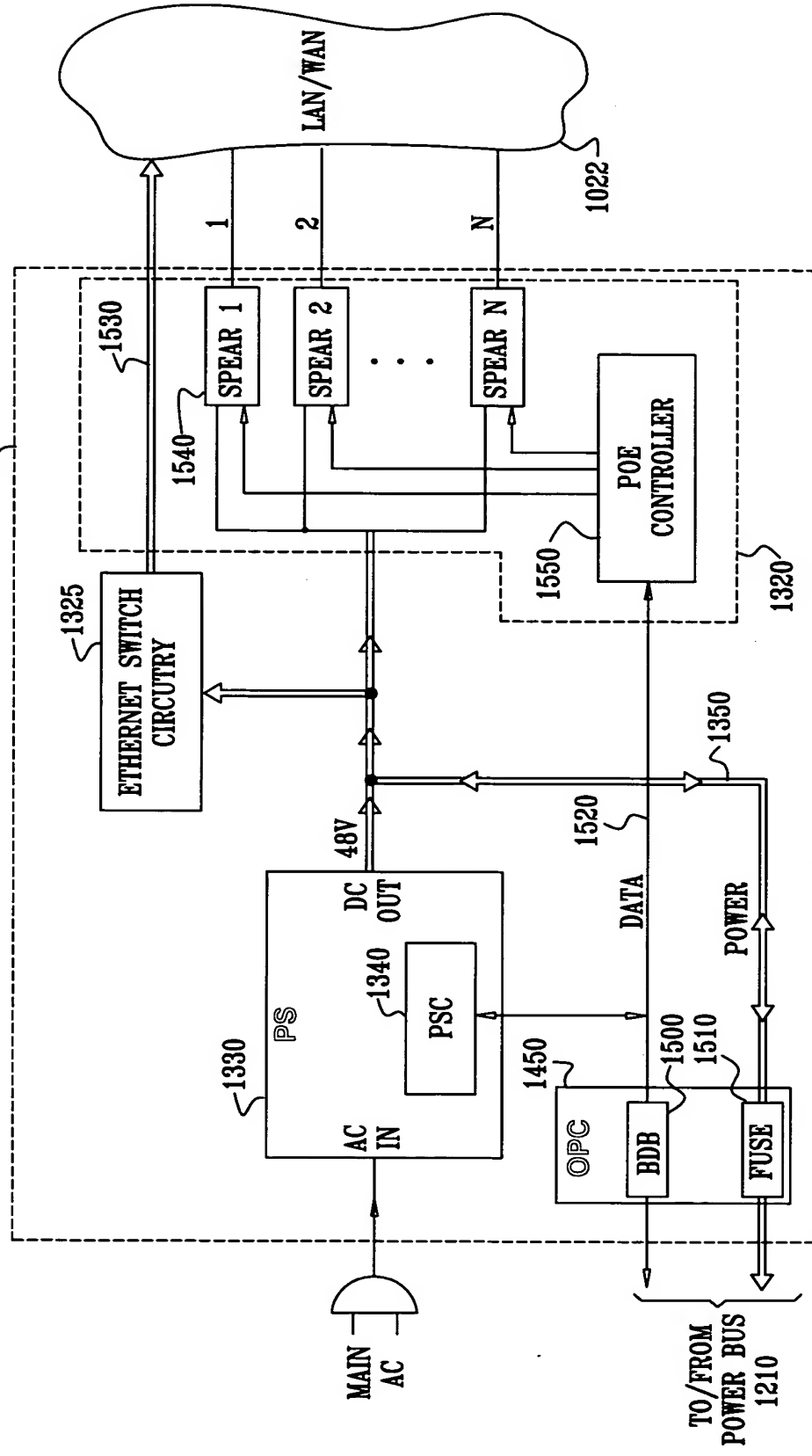


FIG. 21B

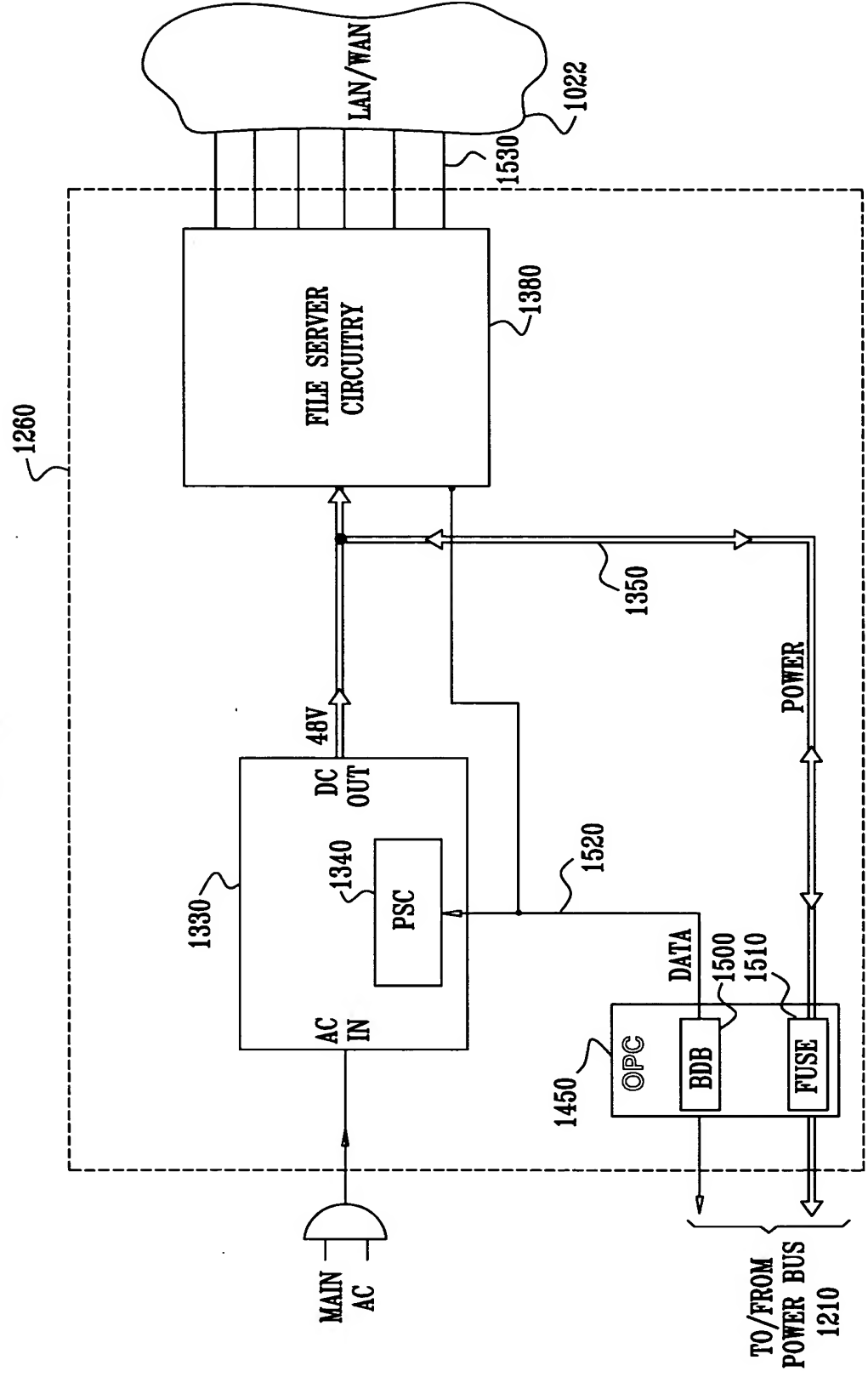


FIG. 21C

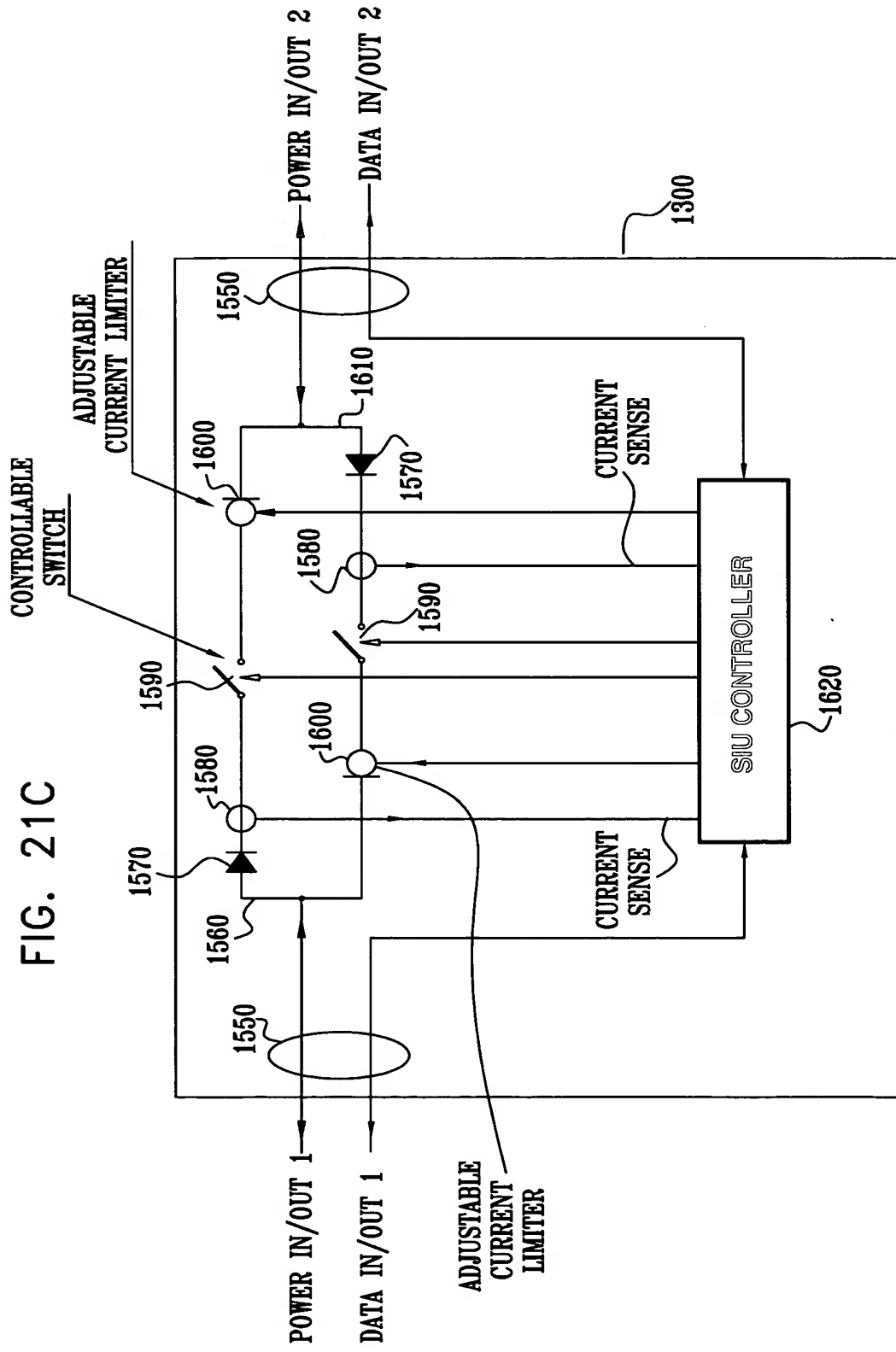


FIG. 21D

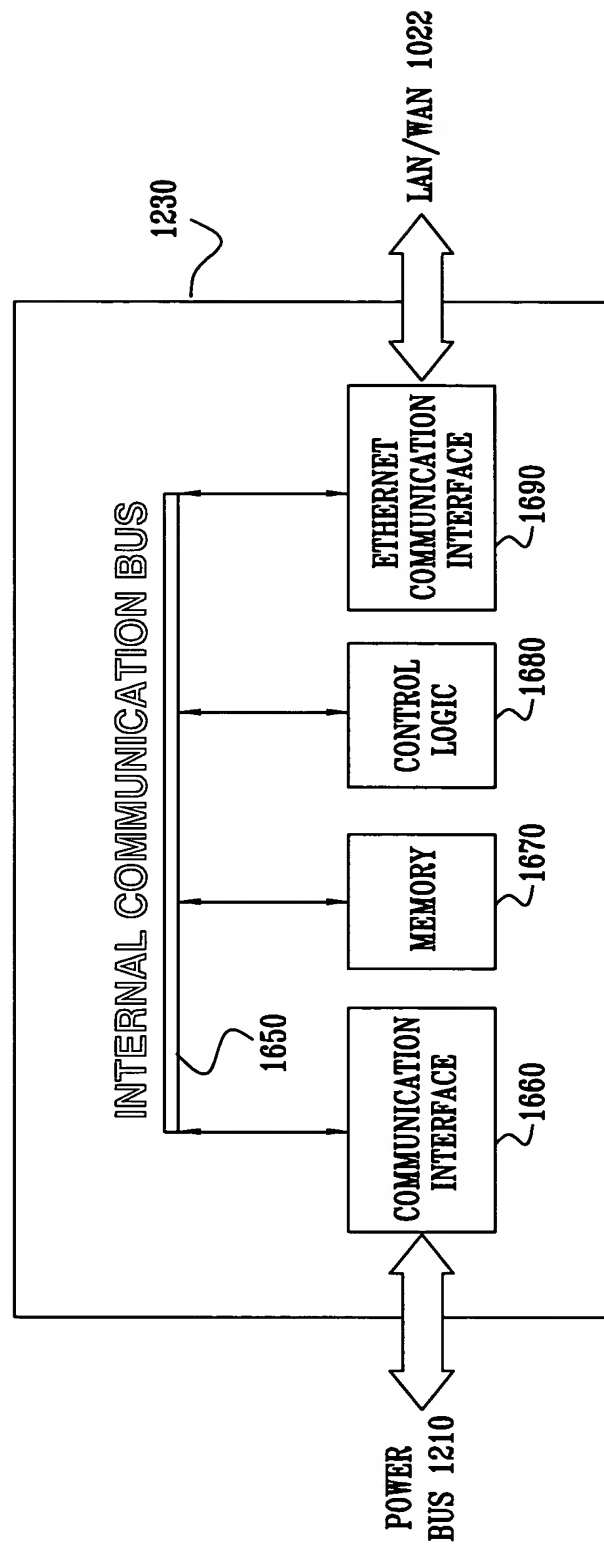


FIG. 21E

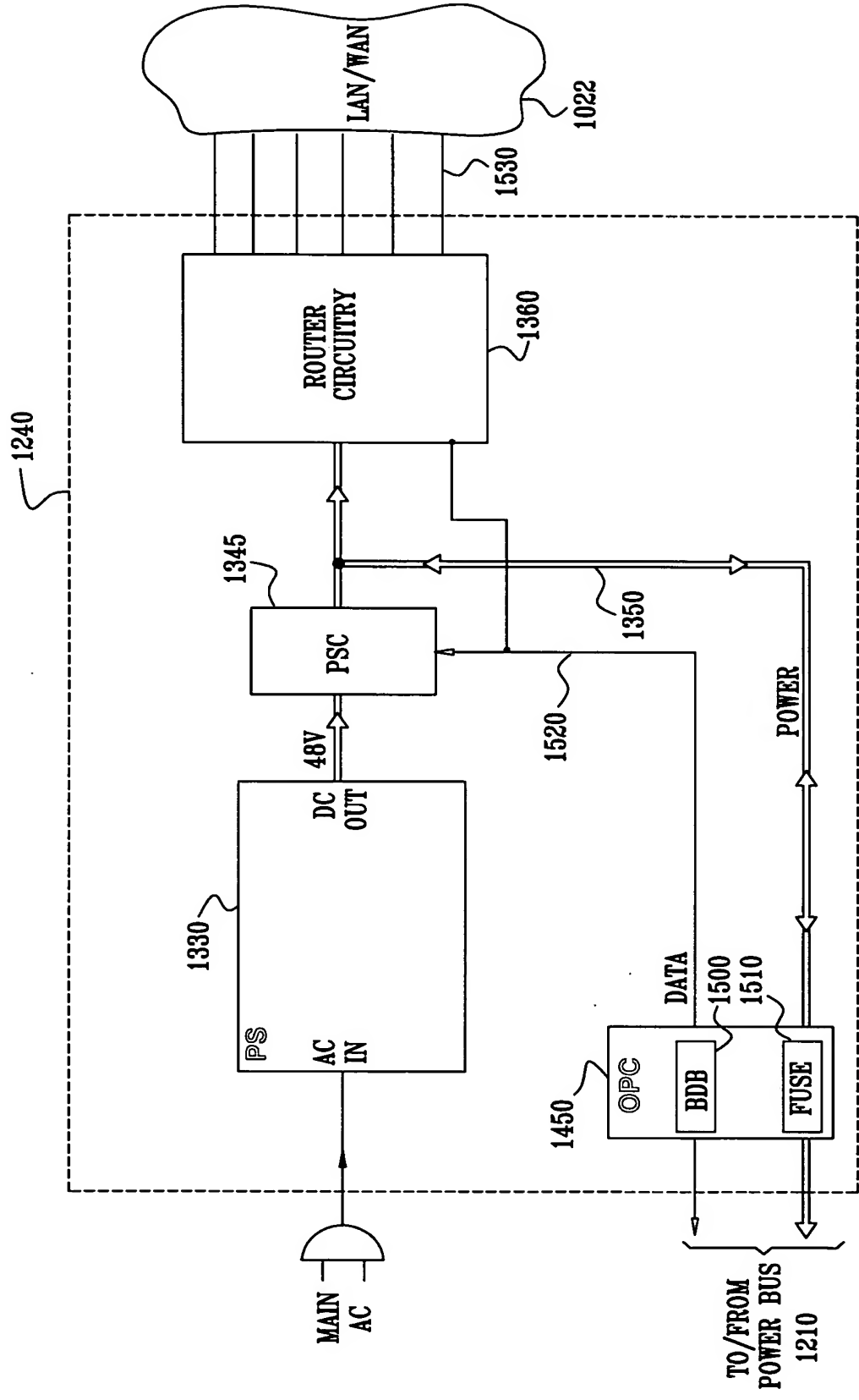


FIG. 21F

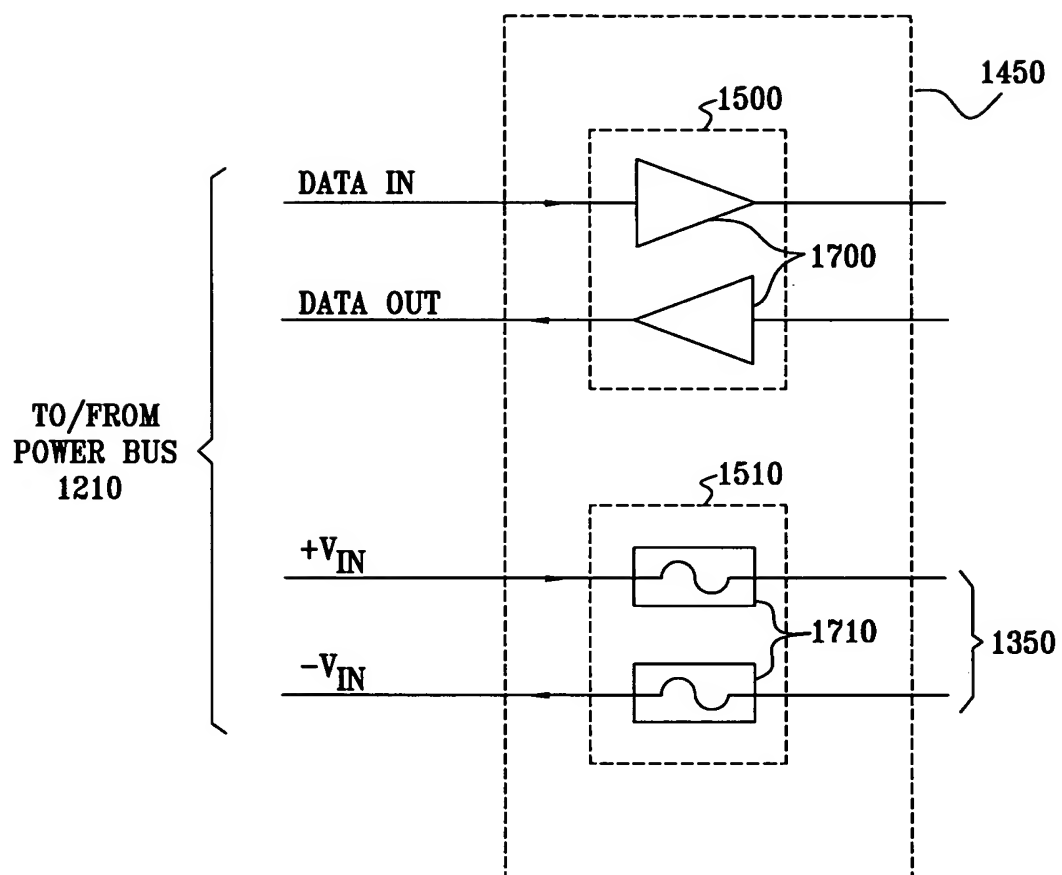
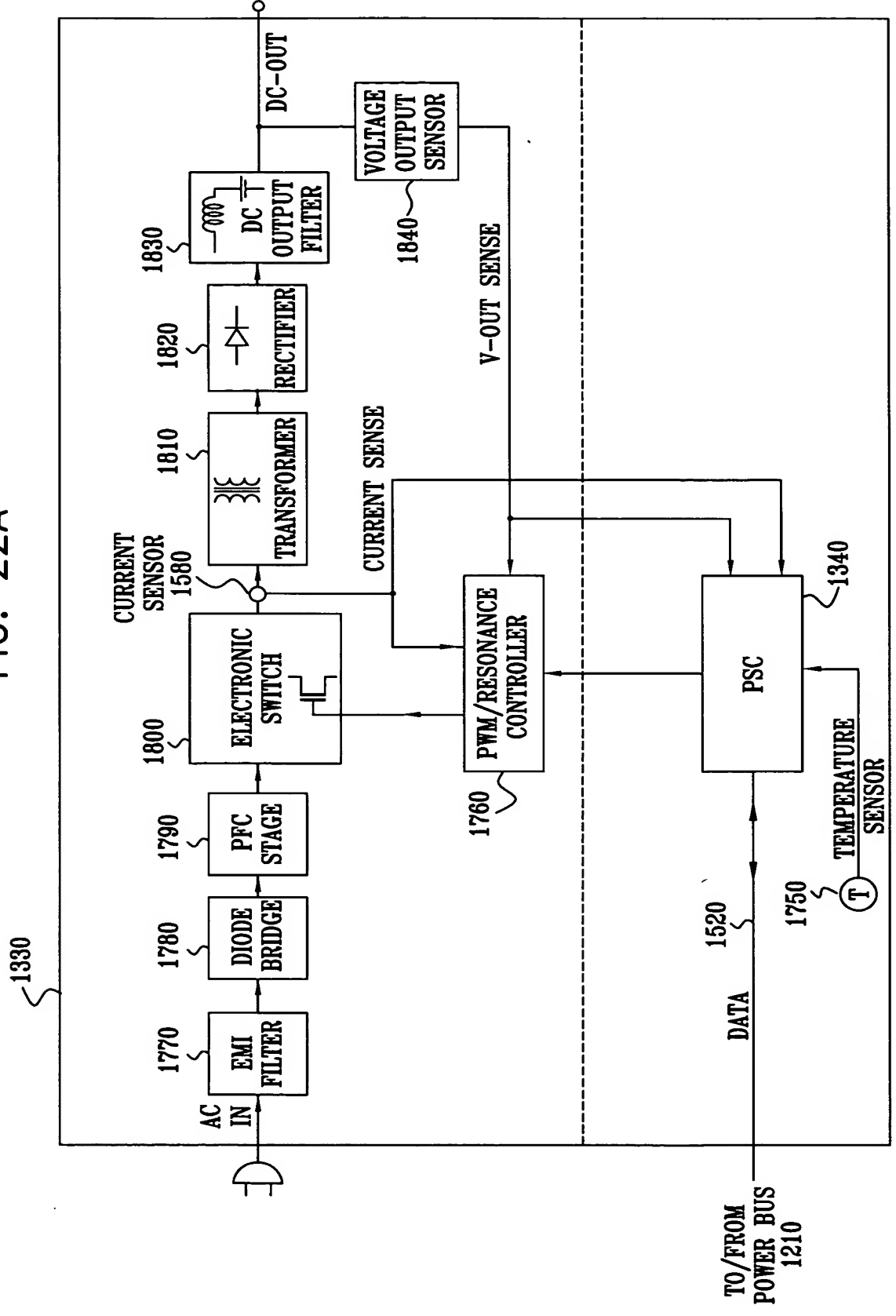


FIG. 22A



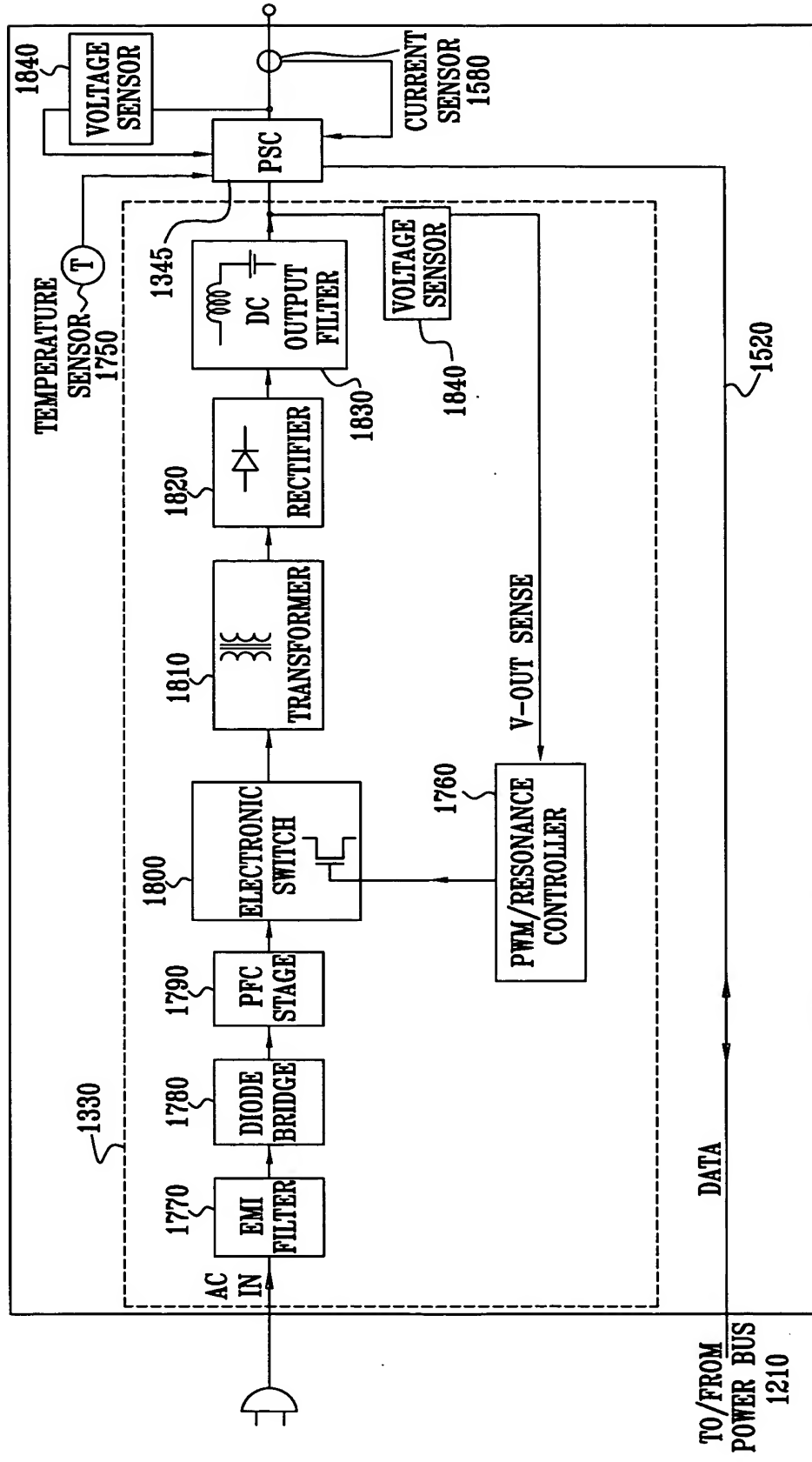


FIG. 23A

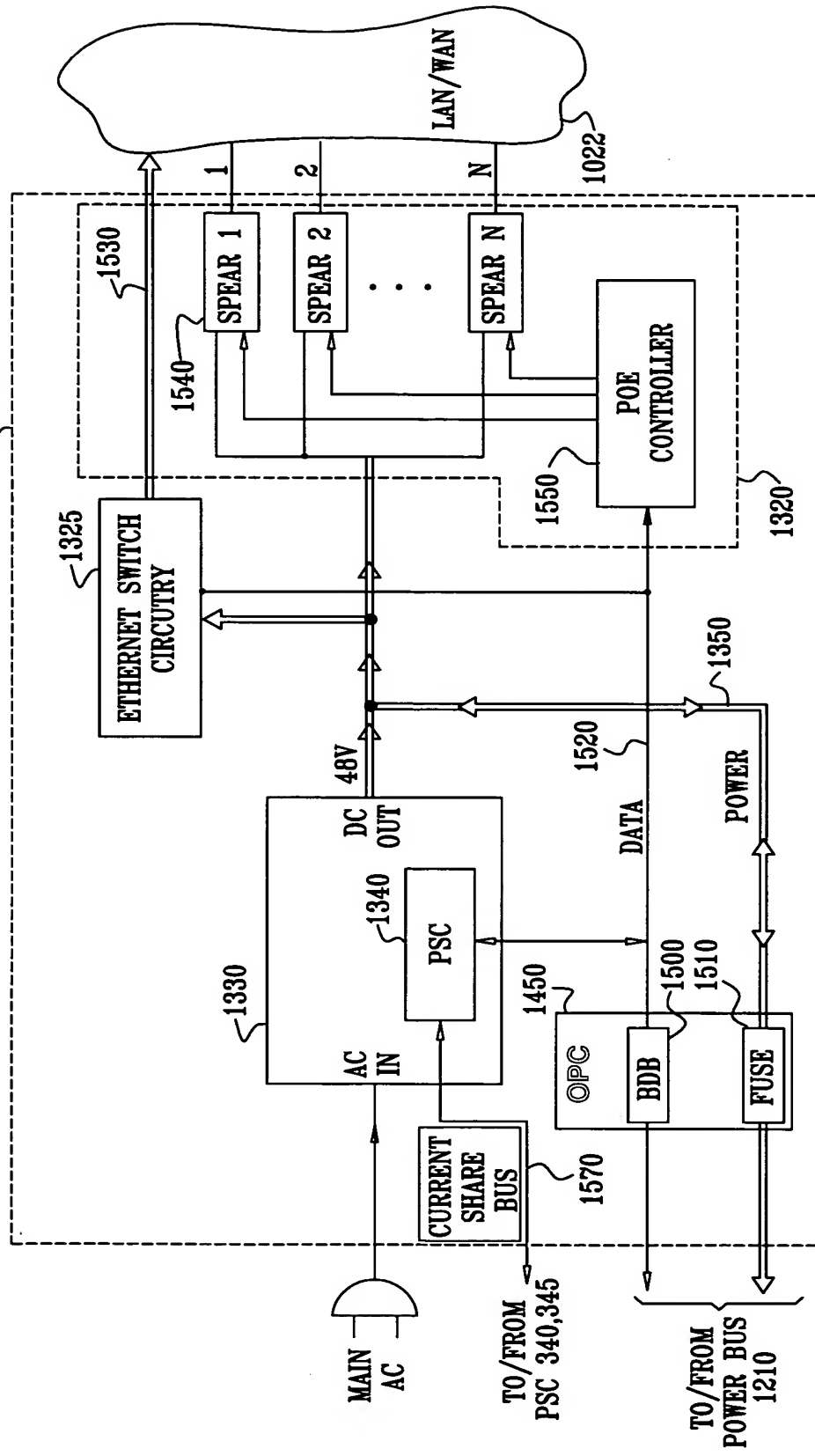
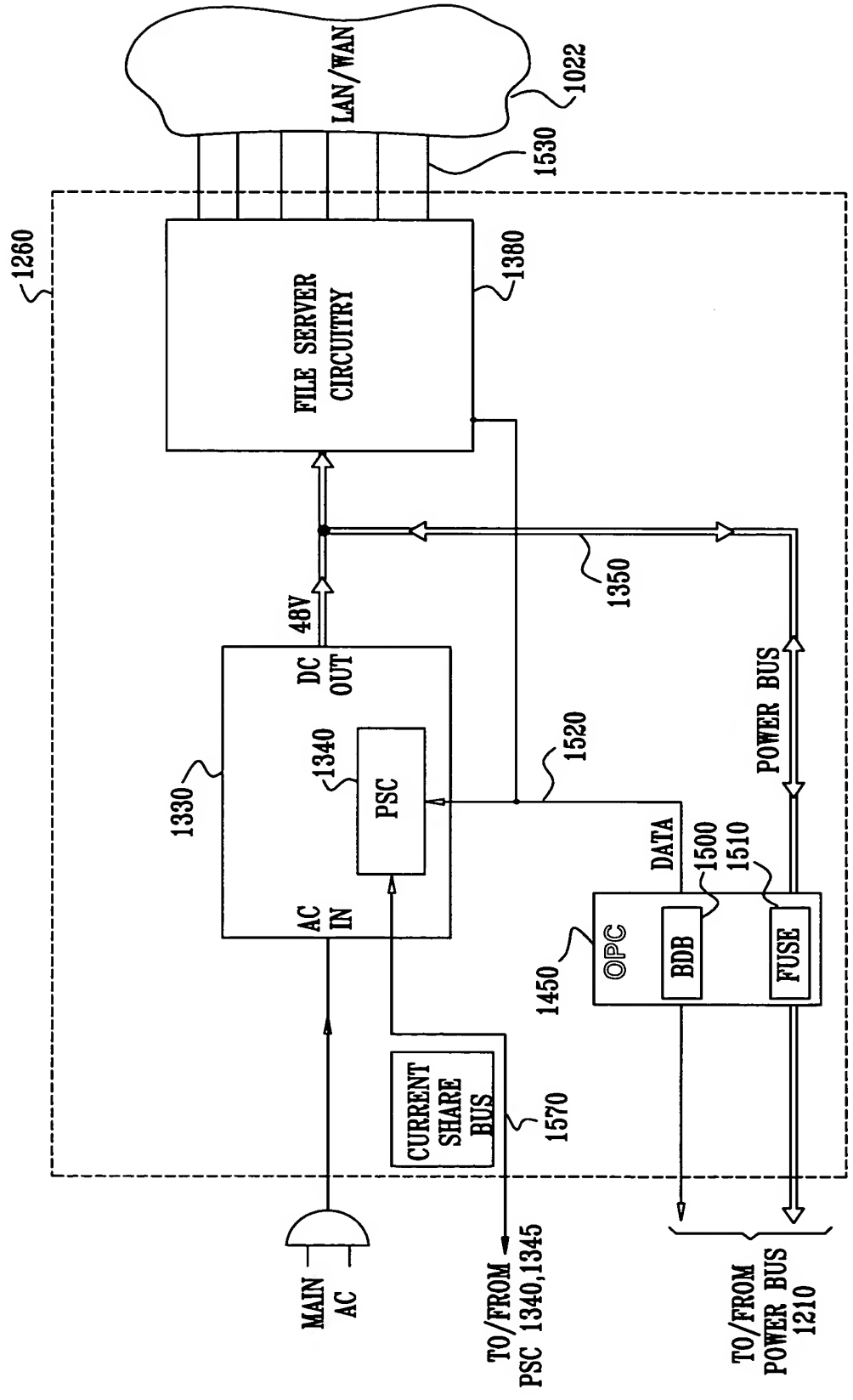


FIG. 23B



The diagram illustrates a power system 1200. It begins with a 'MAIN AC' input connected to a 'PS' (Power Supply) block 1330. The 'PS' block has an 'AC IN' and a 'DC OUT' labeled '48V'. The output of the 'PS' block is connected to a 'PSC' (Power Switching Circuit) block 1345. The 'PSC' block is connected to a 'ROUTER CIRCUITRY' block 1360. The 'ROUTER CIRCUITRY' block is connected to a 'TO LAN/WAN' output 1022. A 'CURRENT SHARE BUS' 1450 is connected to the 'PSC' block and the 'ROUTER CIRCUITRY' block. A 'TO/FROM PSC 1340, 1345' line 1570 is connected to the 'PSC' block. A 'TO/FROM POWER BUS 1210' line 1510 is connected to the 'PSC' block and the 'ROUTER CIRCUITRY' block. A 'DATA' line 1500 is connected to the 'PSC' block and the 'ROUTER CIRCUITRY' block. A 'FUSE' block 1510 is connected to the 'TO/FROM POWER BUS 1210' line. A 'BDB' (Battery Backup Device) block 1500 is connected to the 'DATA' line. An 'OPC' (Output Power Controller) block 1450 is connected to the 'CURRENT SHARE BUS' 1450 and the 'TO/FROM PSC 1340, 1345' line 1570.

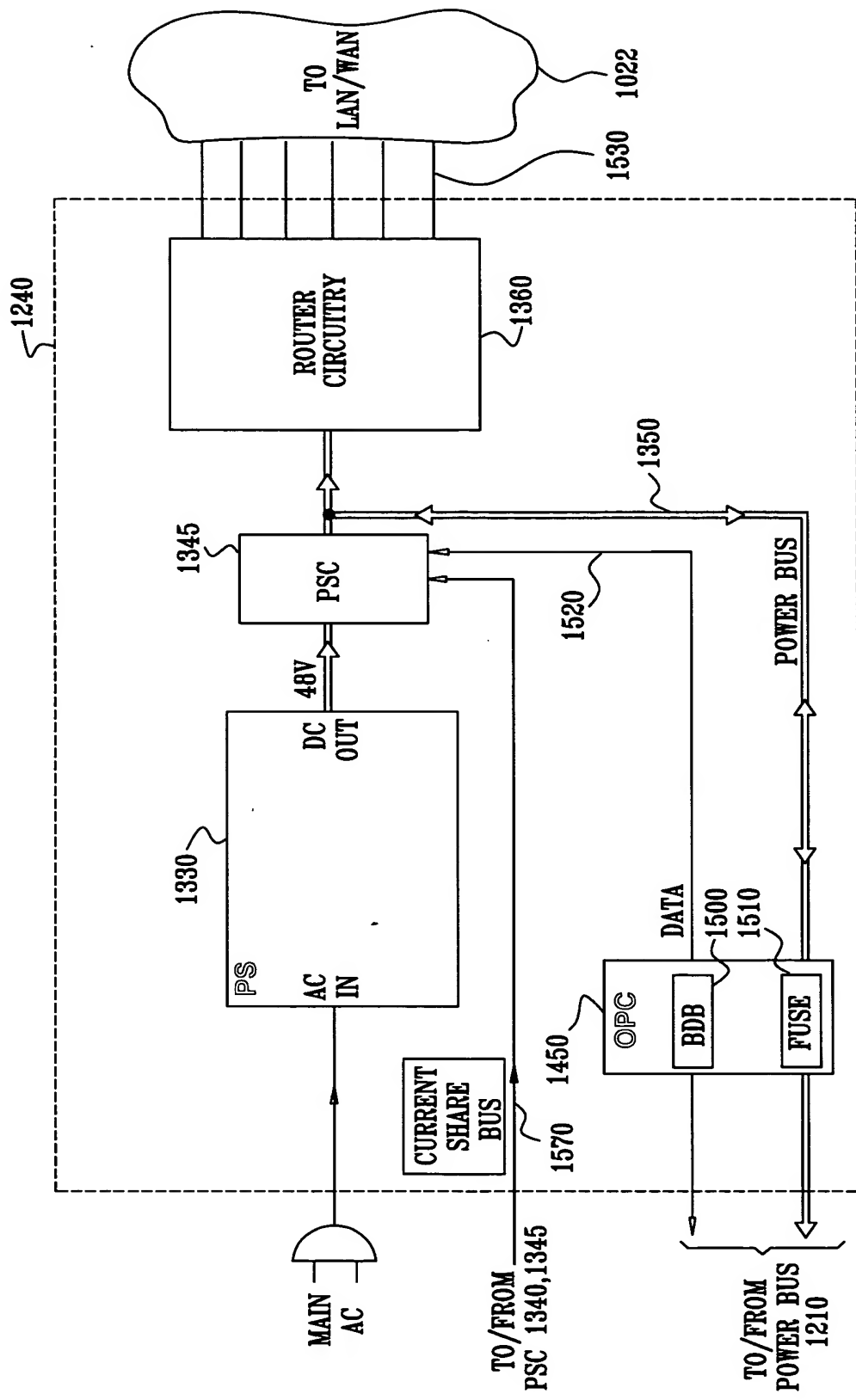


FIG. 24A

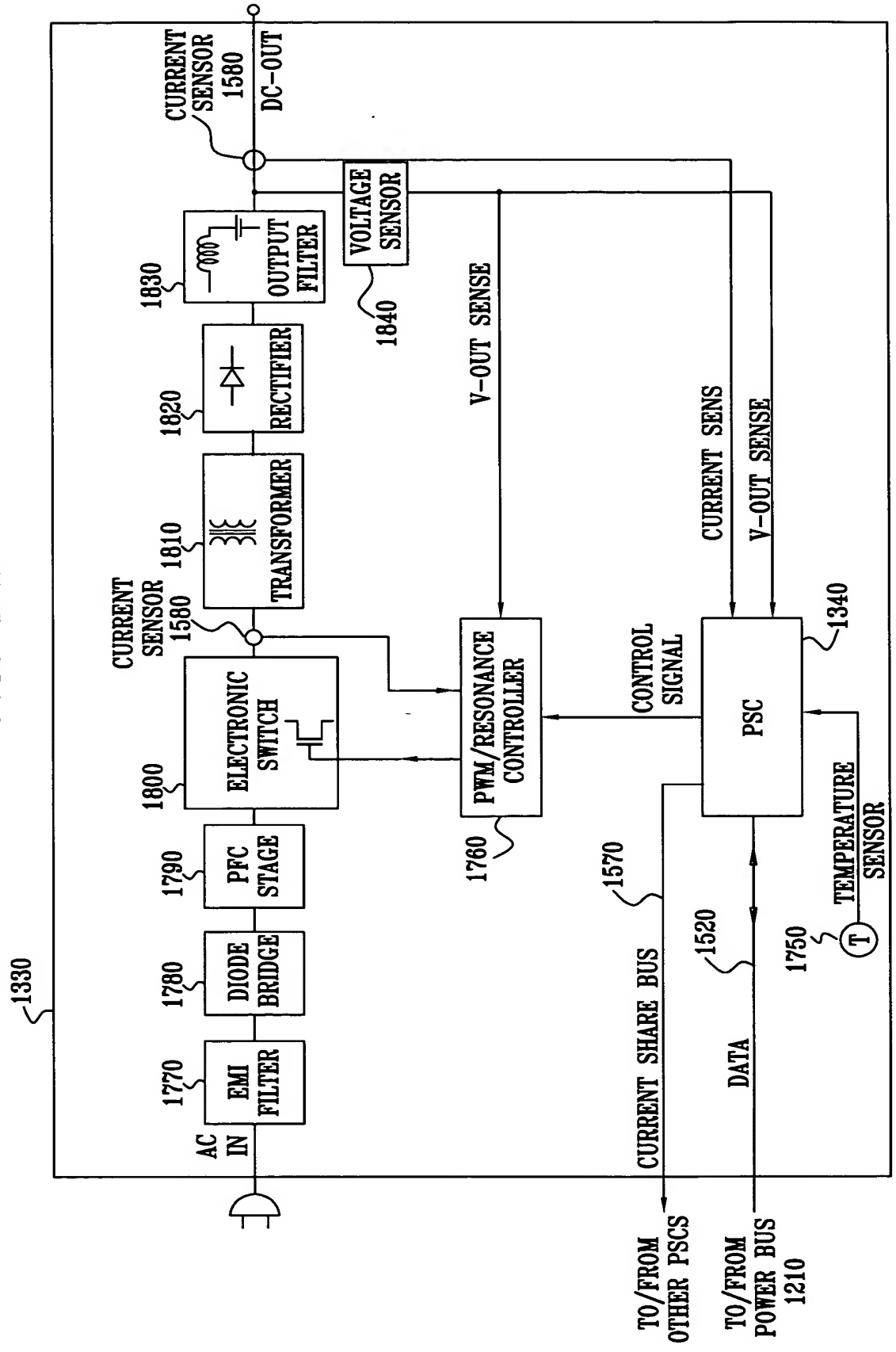
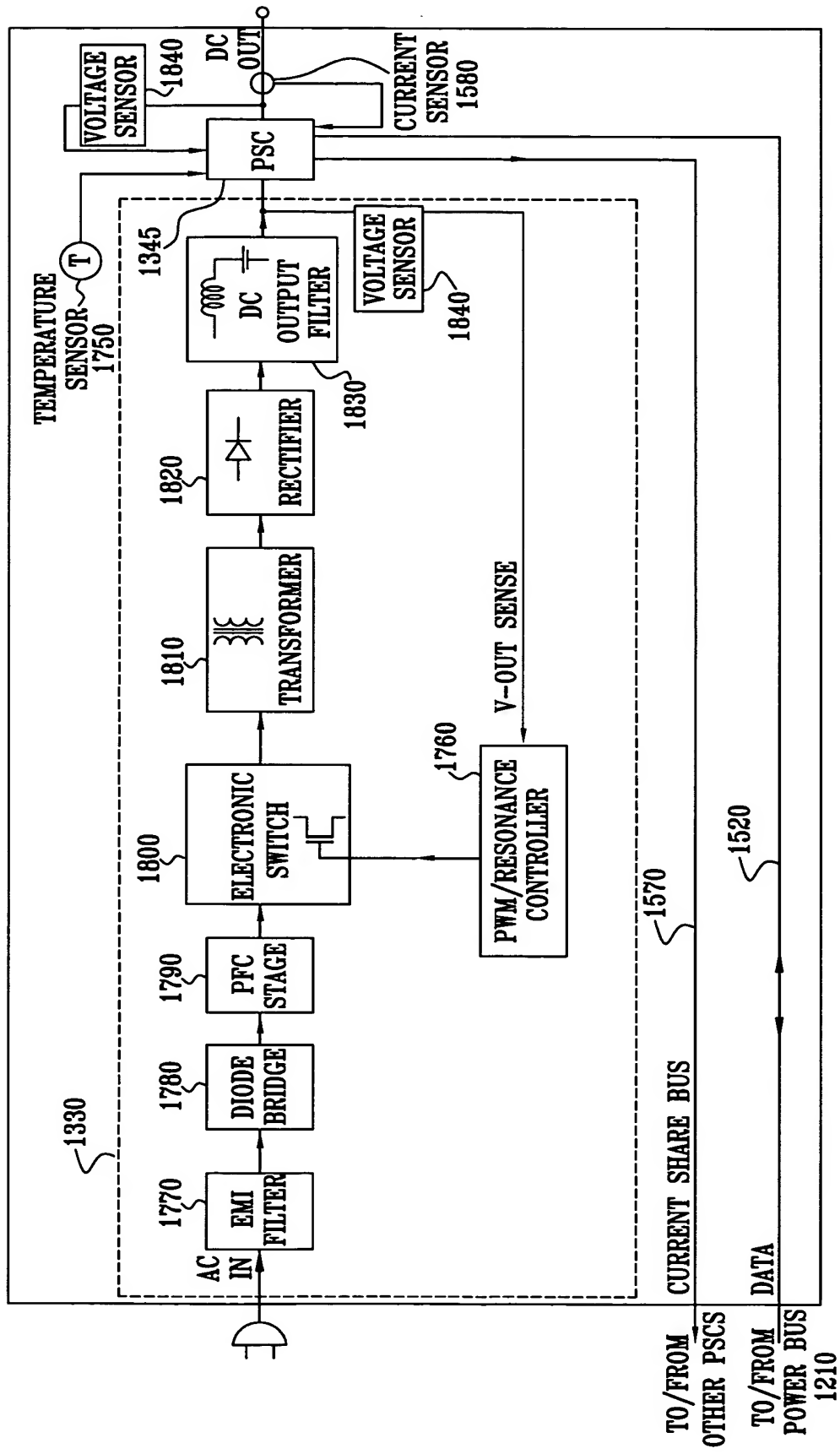


FIG. 24B



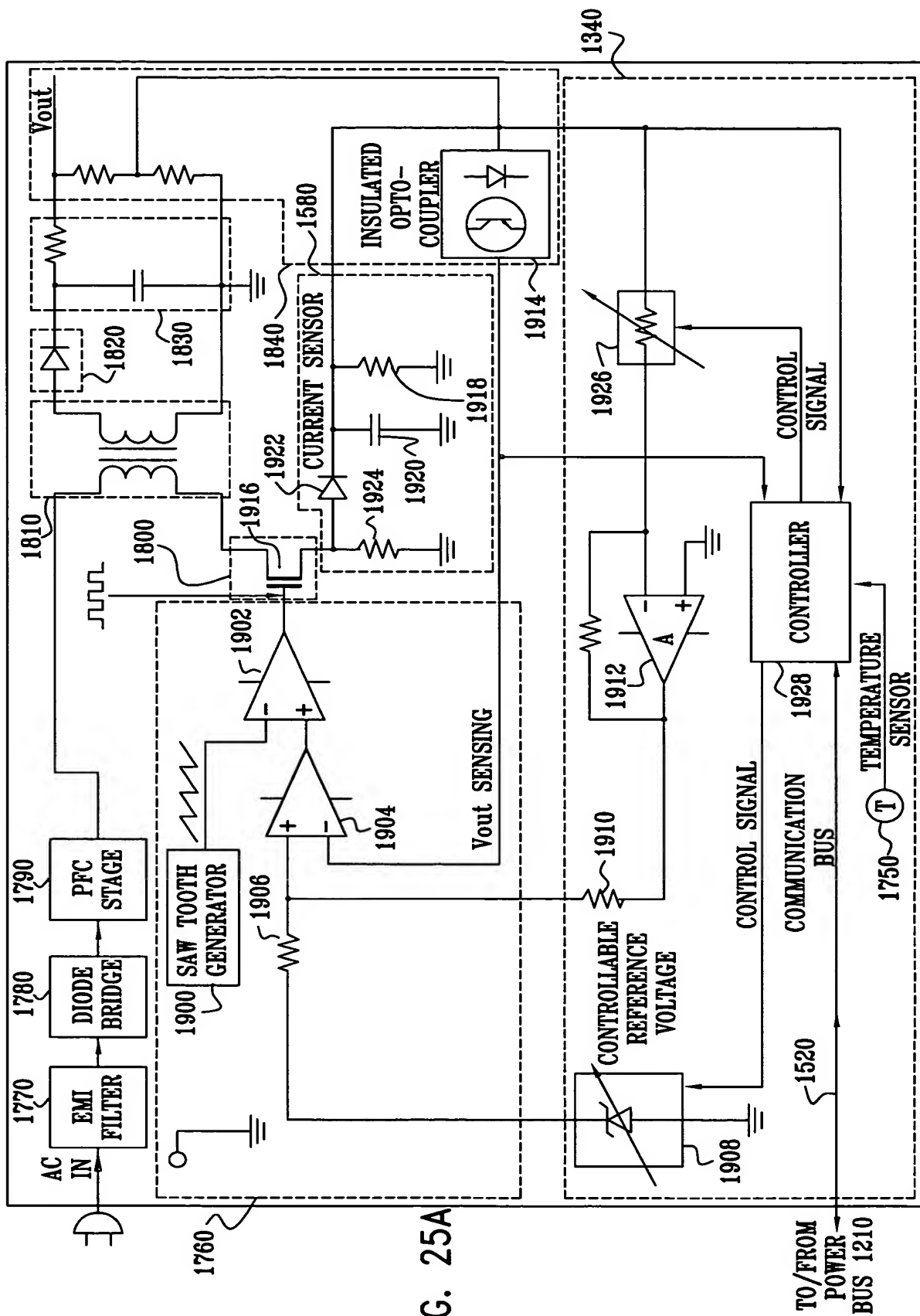


FIG. 25A

FIG. 25B

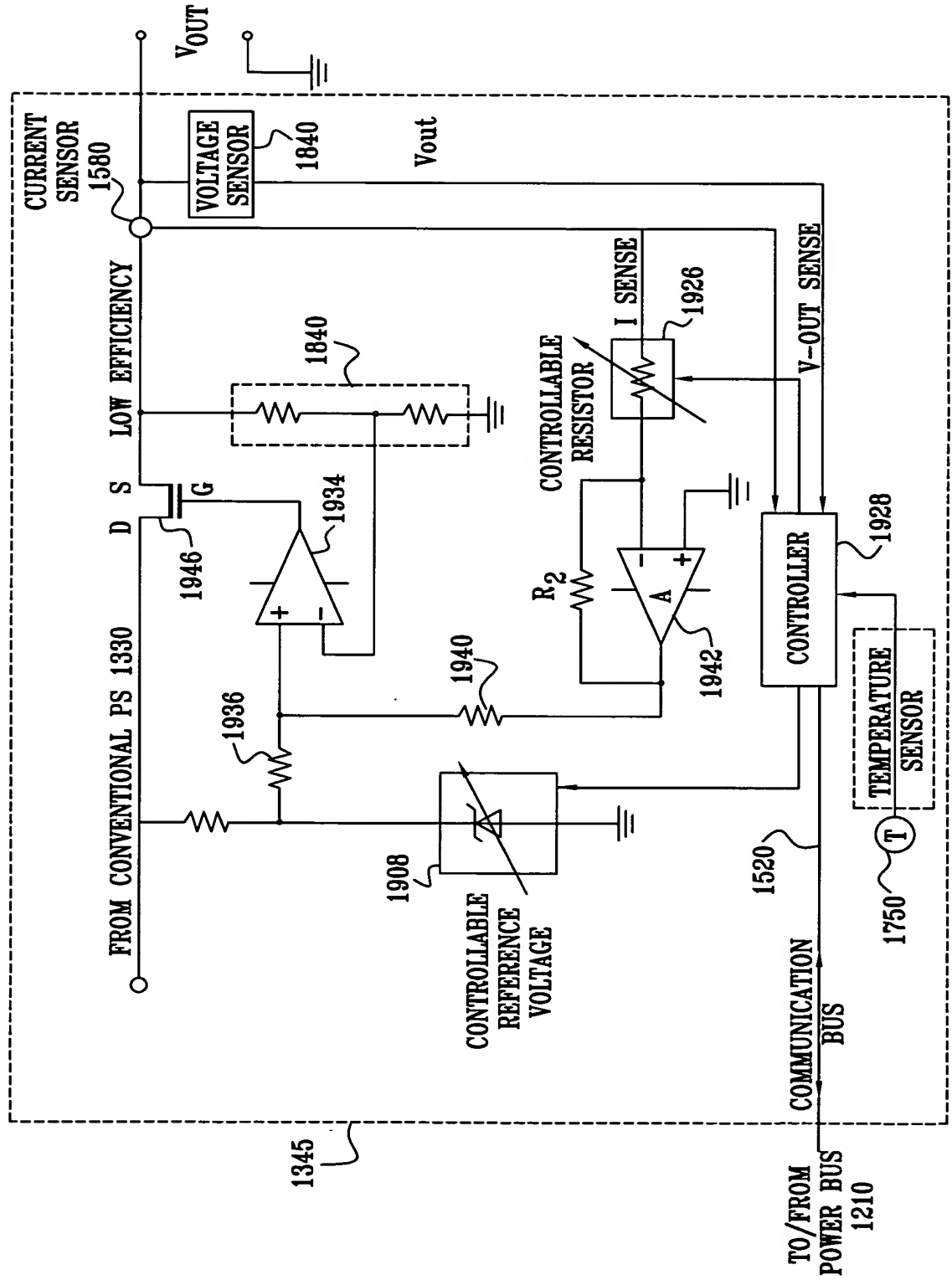


FIG. 25C

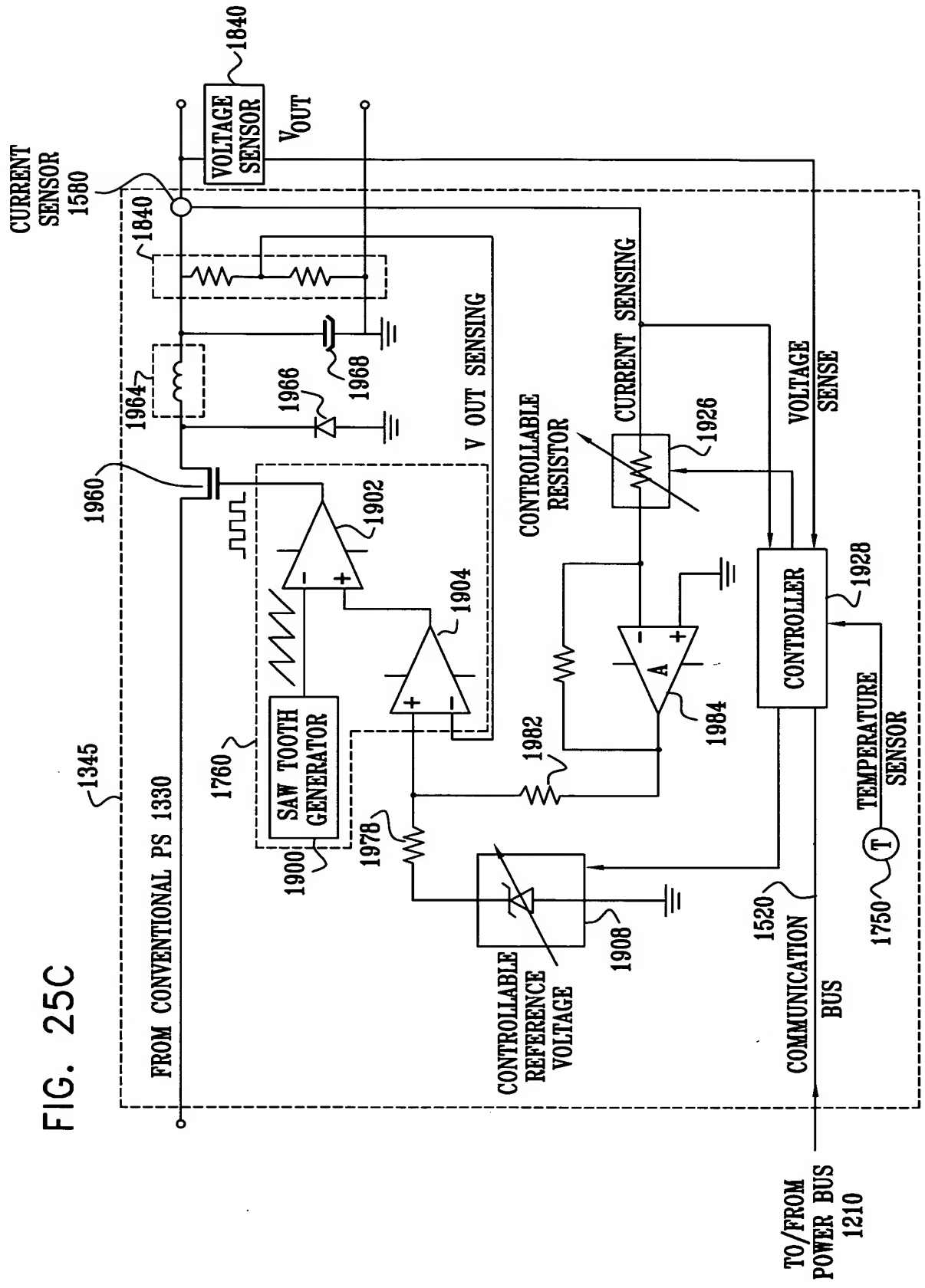


FIG. 26A

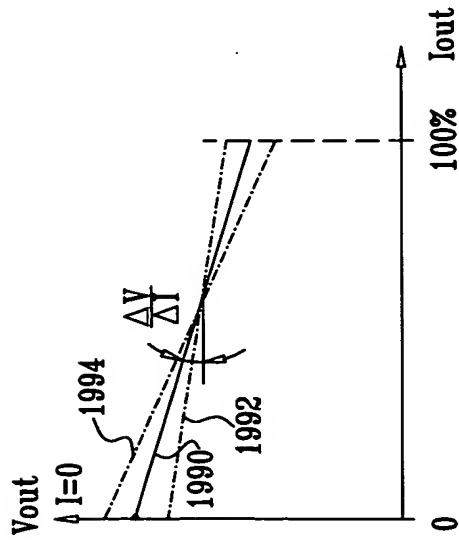


FIG. 26B

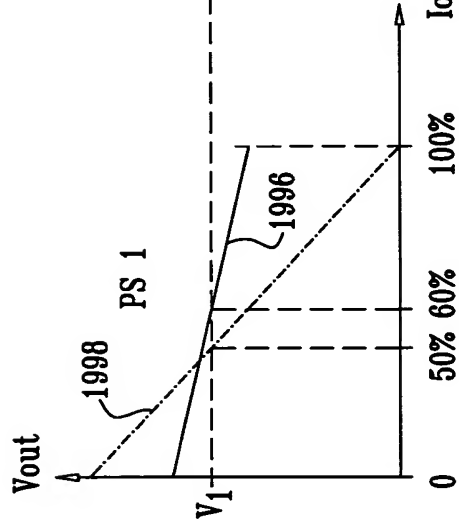
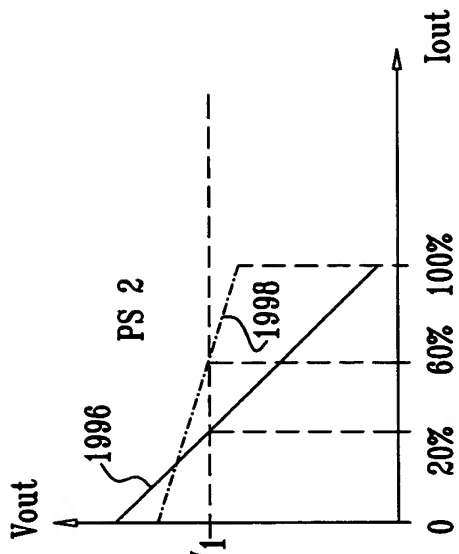


FIG. 26C



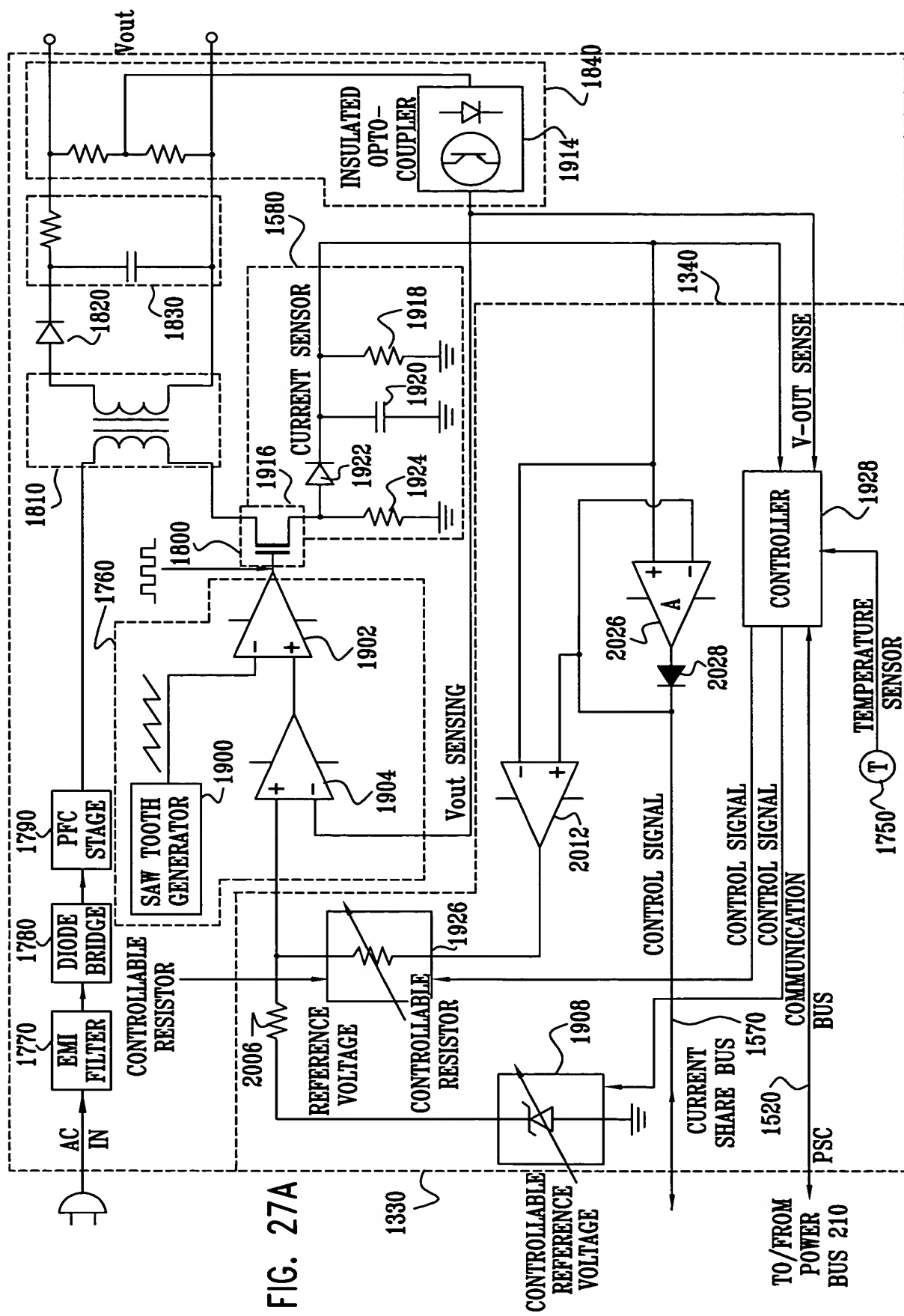


FIG. 27B

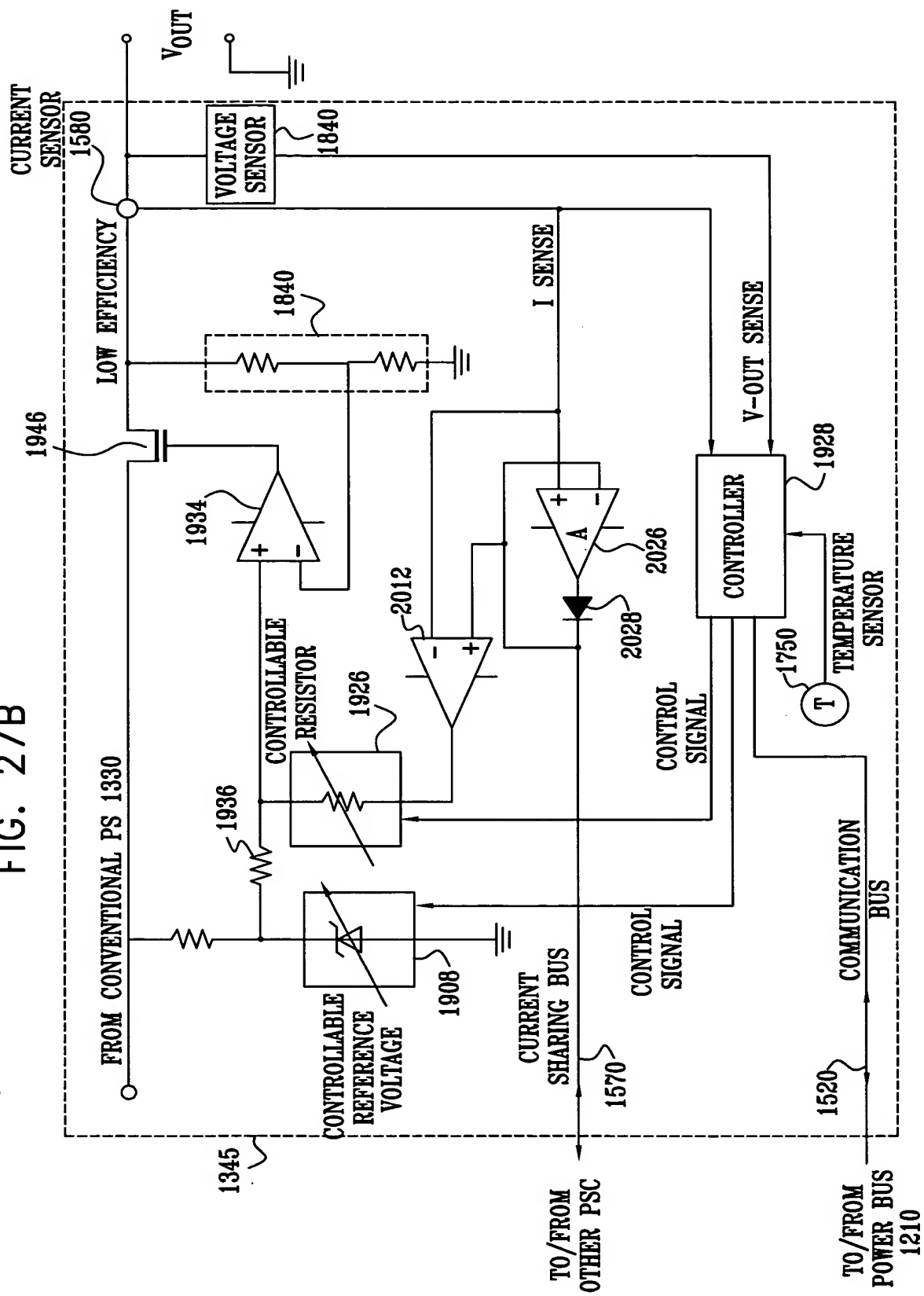


FIG. 27C

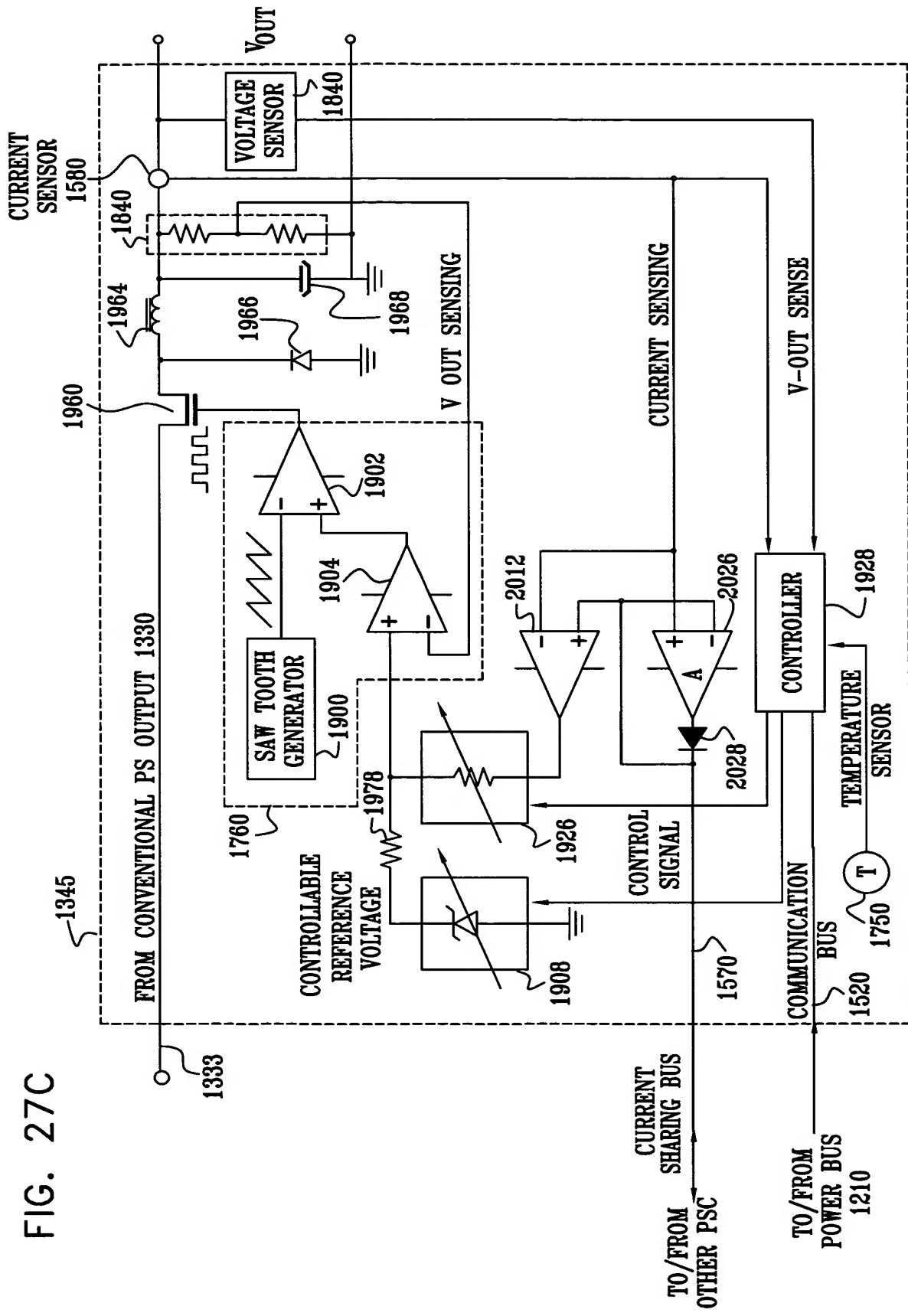


FIG. 27D

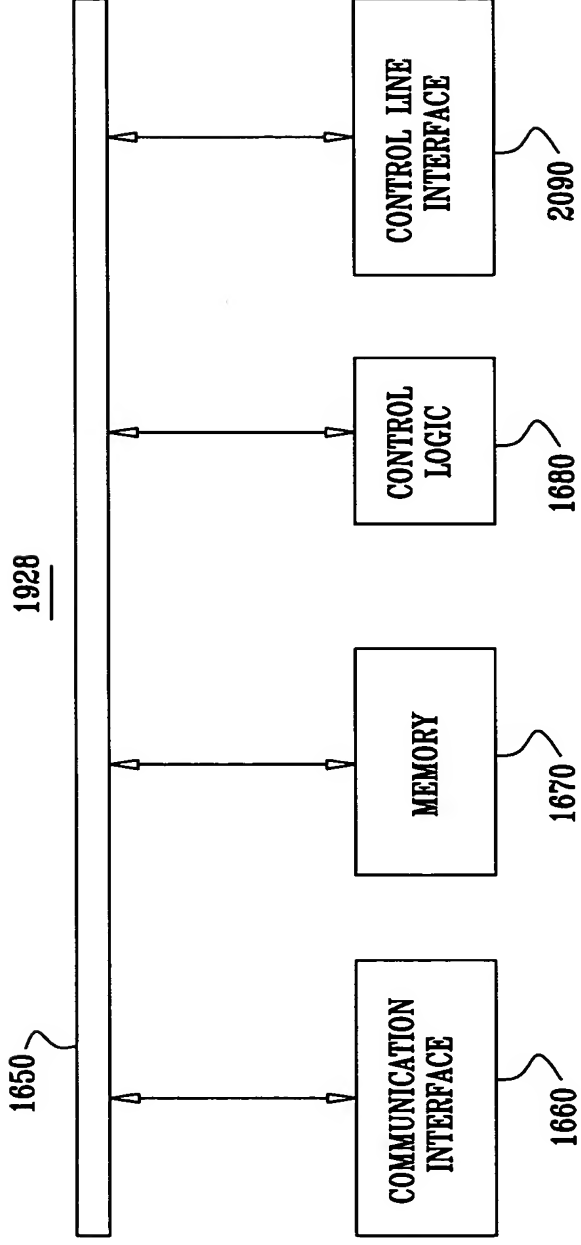


FIG. 28A

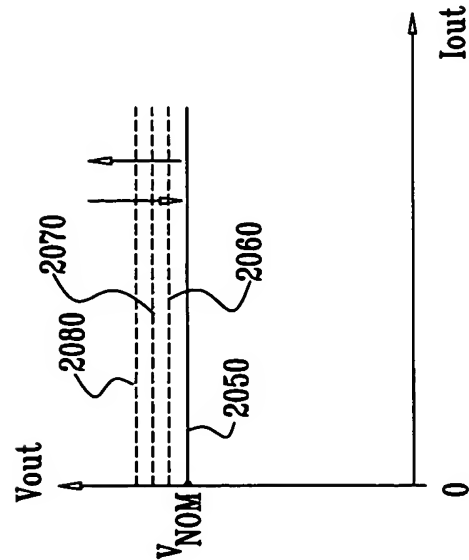


FIG. 28B

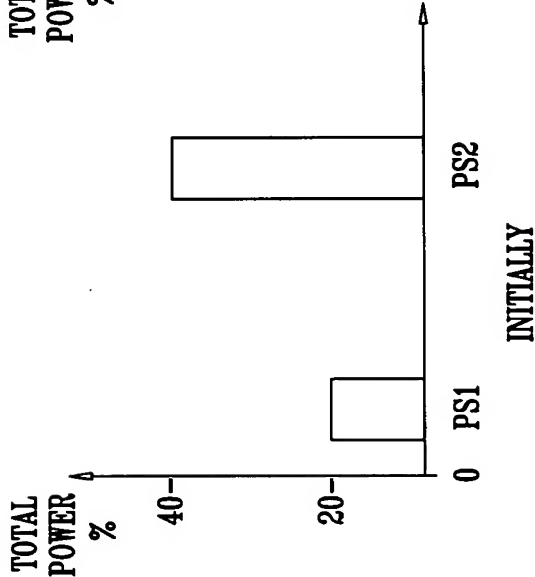


FIG. 28C

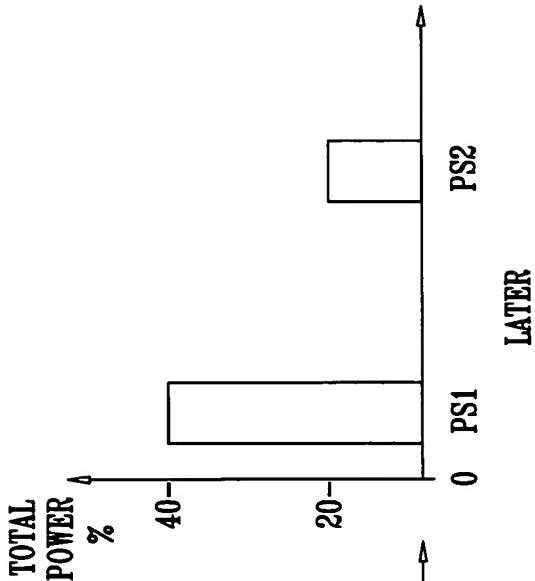


FIG. 29

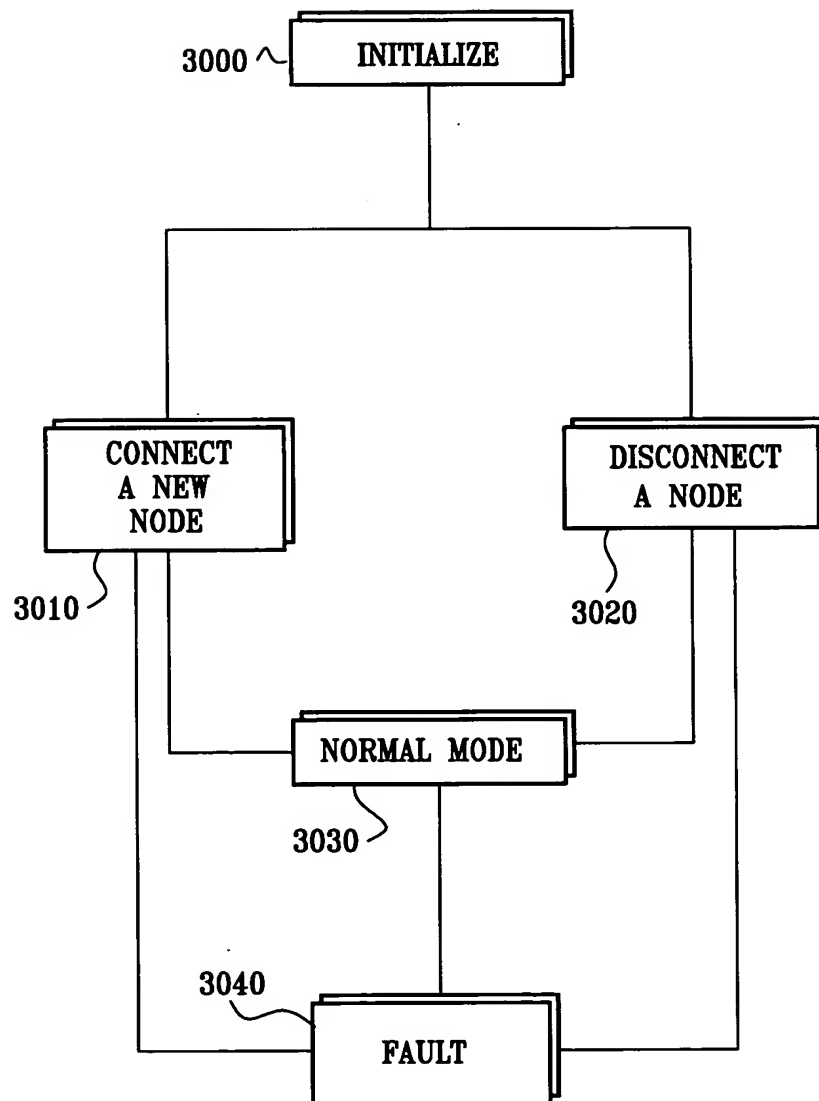


FIG. 30

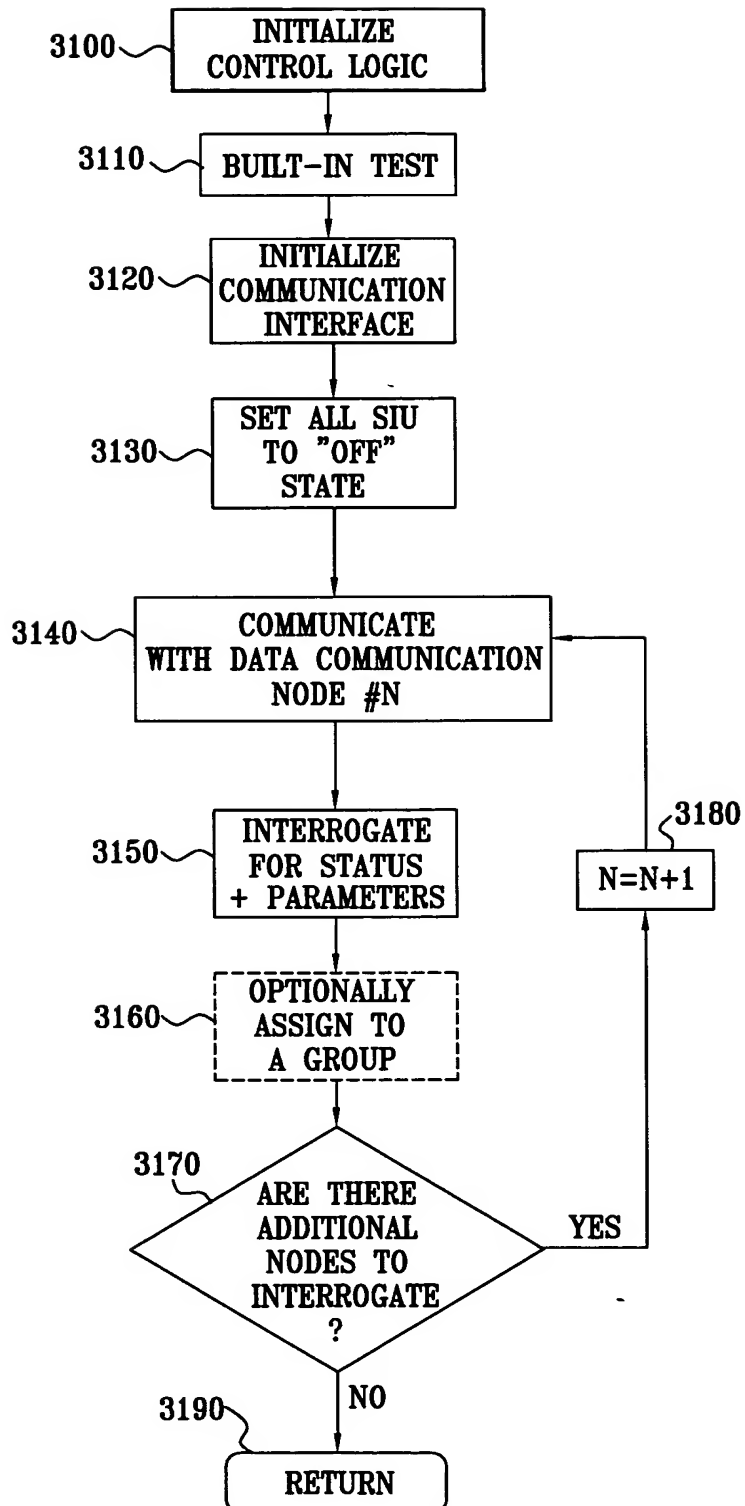


FIG. 31

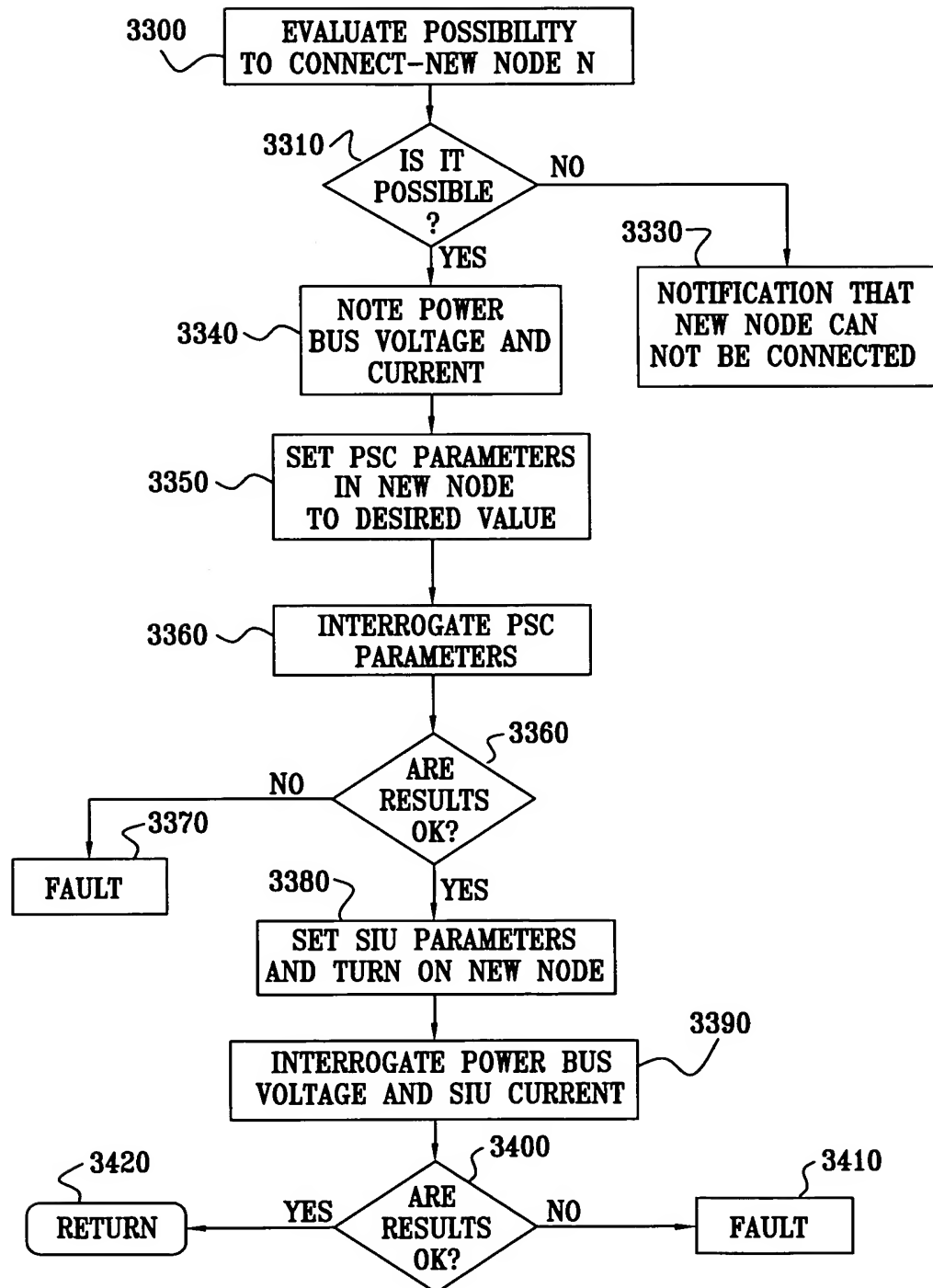


FIG. 32

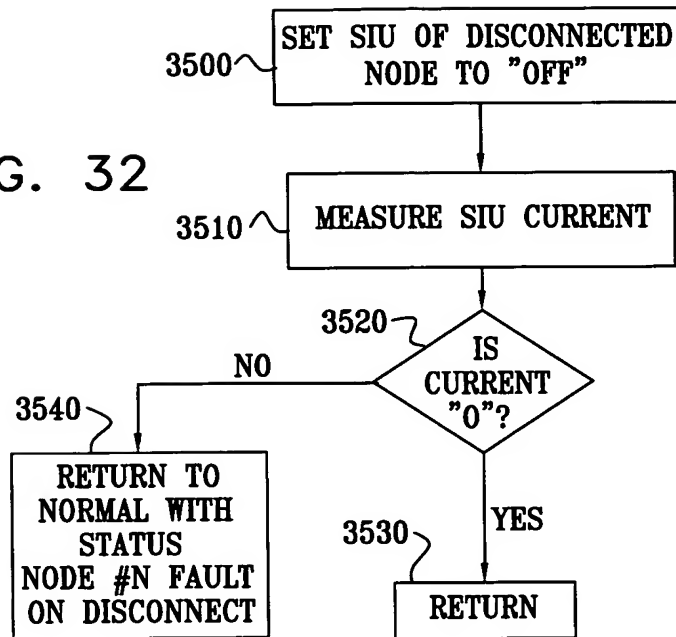


FIG. 33

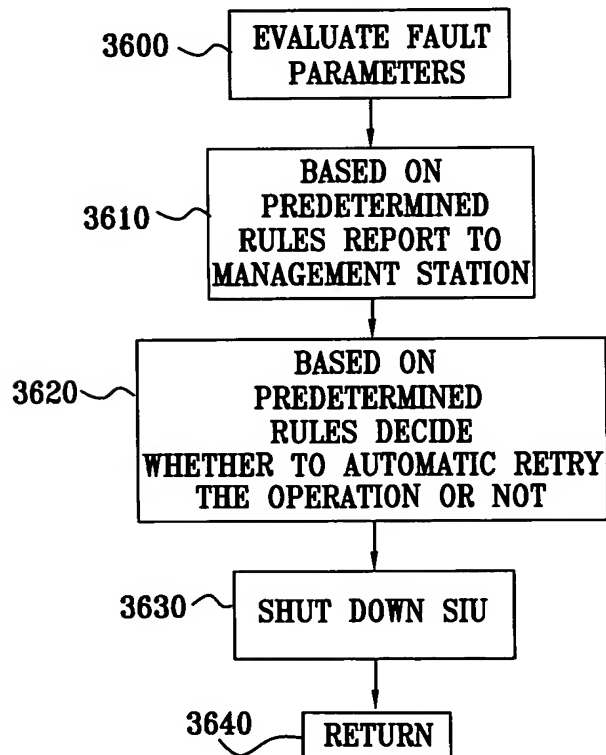


FIG. 34

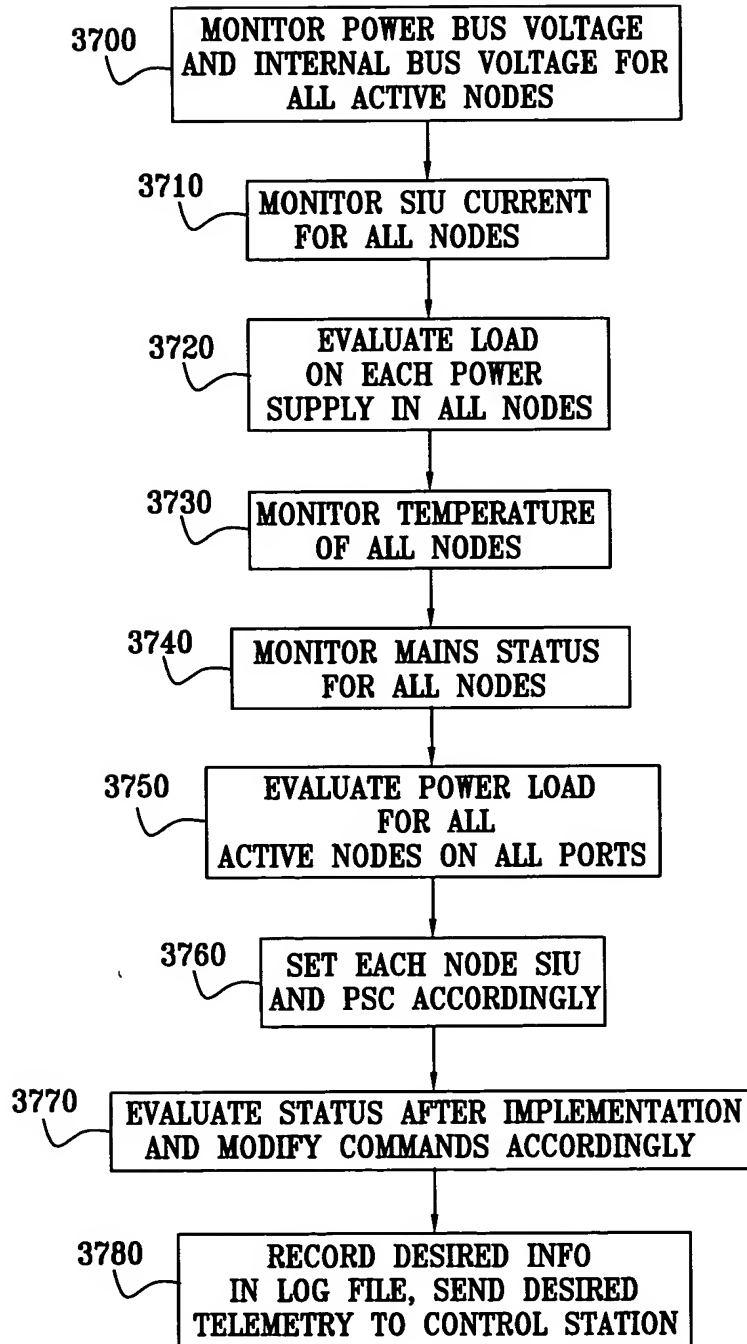


FIG. 35

